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## Top Story

### *Dassault Systèmes Introduces V6 to the Market*

29 May 2008

Dassault Systèmes (DS) announced the market introduction of V6, a next generation PLM 2.0 platform and solution set. Unveiled in January, this sixth generation version was conceived in tight collaboration with industry leaders and leverages the success of DS brands and V5 PLM. V6 harnesses the power of Web 2.0, Web 3D and Dassault Systèmes' rich technology portfolio.

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“PLM 2.0 adopts the concepts of online communities and creation, constituting a new paradigm for product innovation. V6 harnesses collaborative intelligence from diverse online communities to maximize intellectual assets, capturing and leveraging IP from all business and consumer users,” said Bernard Charlès, president and CEO, Dassault Systèmes. “With PLM 2.0 solutions like V6, virtual products and systems behave as they would in the physical world, allowing all actors to have immersive, lifelike experiences in 3D.”

PLM 2.0 is a major redefinition of the PLM markets targeting all users creating, consuming and remixing IP. The first release of Dassault Systèmes’ Version 6, V6R2009, covers business processes designed to serve all of Dassault Systèmes’ 11 target industries. Customers can select from over 150 production-ready products addressing the following domains:

**Lifelike Experience** – 3DVIA V6 enables users to create lifelike experiences for consumers, collaborators and creators to imagine and play.

- **Imagine** – 3DVIA is the first solution ever to put consumers at the core of the product development process. Through 3D-powered lifelike experiences, consumers can try out products and suggest modifications at any stage of the creation process.
- **Play** – 3DVIA helps companies market, promote, sell or maintain their products online with 3D-powered experiences (e.g. advertising, shopping, operation, maintenance, etc.).

**Collaborative Innovation** – ENOVIA V6 provides an open, online collaborative environment, on a single IP management platform, for all product lifecycle activities.

- **Governance** allows companies to manage and monitor entire product lifecycles in an adhoc manner to support global enterprise ecosystems, while remaining on-time and on-budget.
- **Global Sourcing** brings together engineering and purchasing communities to efficiently set up suppliers’ networks, manage sourcing negotiations and monitor supplier performance.
- **IP Lifecycle Management** for the management of all product-related information, such as requirements, recipes, bills of material (BOMs), etc.
- **Unified 3D Live Collaboration** for PLM business intelligence allows communities to collaborate and access all product-related IP in real time. The ENOVIA V6 IP platform provides for the indexing, searching and federating of IP from multiple data sources, such as office documents, ERP or PDM solutions.

**Virtual Design** – CATIA V6 introduces the next generation of collaborative virtual design capabilities across the enterprise.

- **System Engineering** as a collective, integrated multi-disciplinary model for product development. CATIA V6 expands the power of modeling from physical design to a unique RFLP approach, aggregating Requirements, Functional, Logical, and Physical product definitions.
- **Meta-CAD Modeling** delivers the first truly collaborative, liberated design environment. CATIA V6 Modeling delivers breakthrough user experiences and functions to shape and reshape 3D from any modeling source with unsurpassed speed, quality, realism and ease-of-use.
- **Knowledge-Based Design** to get design right the first time by utilizing all company IP (e.g. knowledge, business processes, regulatory requirements, etc.).

**Realistic Simulation** – SIMULIA V6 provides industry communities, from designers to simulation

specialists, with a unique collaborative environment to perform lifelike simulation and virtual product behavior testing.

- **Multiphysics Digital Lab** delivers a unique virtual hub that supports industry's extreme diversity of multiphysics simulation requirements and, therefore, dramatically reduces the need for physical testing.
- **Compliance** ensures an unmatched level of trust in digital simulation results and assets, thanks to a portfolio of leading-edge technologies.
- **Open Scientific Platform** supports all simulation-related product development and scientific research practices. The SIMULIA V6 open scientific platform is the foundation for the development of Dassault Systèmes' lifelike simulation ecosystem.

**Digital Manufacturing and Production** – DELMIA V6 delivers next generation PLM 2.0 manufacturing solutions to create, share, execute and optimize virtual production systems.

- **Manufacturing Planning** supplies the entire supply chain with comprehensive 3D process and resource planning solutions to create and optimize build-to-order and lean production manufacturing systems.
- **Plant & Resource Engineering** provides the tools to define and optimize manufacturing assets concurrently with manufacturing planning, leveraging DELMIA's unique Product Process Resource (PPR) model.
- **Program & Control Engineering** to virtually program, validate and simulate manufacturing systems for the virtual commissioning of production facilities.

CATIA, DELMIA and SIMULIA V6R2009 are native PLM 2.0 online solutions with a patent pending, unified user interface. They fully leverage the power of the ENOVIA collaborative IP platform. V62009 provides a solid foundation for Dassault Systèmes' PLM 2.0 ecosystem extension. For more information, visit <http://www.3ds.com/V6>.

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## Acquisitions

### *Bentley Acquires Common Point to Mainstream Construction Simulation*

29 May 2008

Bentley Systems, Incorporated announced that it has acquired the global business of Common Point, Incorporated, a pioneer and market leader in software products for construction simulation (ConstructSim) and operations simulation (OpSim). ConstructSim has become widely established in plant projects, has been successfully deployed on building projects, and, along with OpSim, can also be applied in civil infrastructure projects. The integration of Common Point's technology with Bentley's ProjectWise Navigator platform and comprehensive portfolio of applications and collaboration servers will enable Bentley to accelerate integrated project delivery for infrastructure projects by closing gaps between design, construction, and operations.

About this acquisition, Pieter Diedericks, senior project director, Oil Sands Projects, Petro-Canada, said, "As an owner faced with the challenges of managing our mega projects from end to end across multiple EPCs, we are delighted that Bentley has acquired Common Point to address the construction side of our business. Some time ago, we standardized on Bentley software for engineering design, and we used

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Common Point's ConstructSim for construction simulations. We are excited that this acquisition promises improved integration between engineering and construction that will deliver a further step change in project productivity and efficiency.”

ConstructSim, Common Point's flagship offering, is the first software to model the construction process and provide a construction management environment that links data from design deliverables, schematics, project management, materials, and resource management systems in a virtual and visual model. This linked model allows users to create and track work packages that closely match the actual construction workflow, enabling project managers to more quickly identify issues with time-critical activities or incorrect materials and avoid costly mistakes, schedule overruns, and change orders. This unique “work-facing” orientation of ConstructSim provides an ideal tool for the construction project superintendent or field crew supervisor to organize, manage, schedule, and record the progress of actual installation processes and tasks.

ConstructSim provides vital connections through the downstream construction processes for major infrastructure projects and is viewed as a critical component for integrated project delivery. It has been adopted by leading owner-operators, including BP, ExxonMobil, Rohm and Haas, and Chevron, and also by leading engineering/procurement/construction (EPC) firms such as Jacobs Engineering Group, Fluor, and Zachry Construction. The OpSim product suite is used for the training of operations and maintenance personnel, which can begin before construction of facilities has been completed, through virtual and visual models.

Since its founding in 1999, Common Point has benefited greatly from far-sighted early investments by Zachry (<http://www.zachry.com>), a leading engineering/construction firm with over 18,500 employees worldwide. Zachry additionally served to validate ConstructSim's breakthrough modules for steel and piping work packaging.

Zachry Senior Vice President and Common Point Board Member Ed Bardgett said, “It has been exciting for Zachry to contribute to and benefit from our industry's advancements in software for construction simulation, which we and many others now rely upon extensively to reduce risk and improve project performance. I also congratulate Ted Blackmon and Bill McSherry of Common Point, along with their colleagues, for reaching this significant inflection point in widespread adoption of their visionary technology.”

Zachry CIO Norm Thurow added, “We are thrilled that Bentley will be incorporating ConstructSim and OpSim throughout its solution offerings.”

Commenting on this latest Bentley acquisition, Bhupinder Singh, senior vice president, Bentley Software, said, “We look forward to integrating Common Point's technology with Bentley's ProjectWise Navigator platform and comprehensive portfolio of applications and collaboration servers to address the construction and operations of infrastructure projects. The acquisition perfectly leverages the applied research we have prioritized for the last year to support work packaging. This combination will enable infrastructure projects to proactively surmount design-for-construction issues, in particular, through ConstructSim's unique construction simulation capabilities.”

Ted Blackmon, former president and CTO of Common Point, now Bentley director, Construction Solutions, said, “We are excited about becoming members of the Bentley team as we continue to pursue the vision we have for virtual and visual model technologies. The depth and reach of Bentley's integrated portfolio and its market position, coupled with its unique strength in collaboration servers, will accelerate the deployment of ConstructSim and OpSim throughout the world of infrastructure projects, to the benefit of their successful delivery.”

## About ConstructSim

ConstructSim allows users to aggregate and re-aggregate design, estimating, material, and resource information from many different systems and sources in a unique visual environment. It generates a comprehensive, linked model of the construction process that allows users to create and track work packages that closely match the actual construction workflow. The system can generate schedule simulations, status reports, and work-in-progress punch lists based on available materials and resources as well as dependencies for a particular work package. The comprehensive end-to-end capabilities of ConstructSim enable it to track the construction process closely enough to provide not only timely updates to change orders but also progress tracking.

For more information about Bentley's ConstructSim and OpSim products, visit <http://www.bentley.com/en-US/Corporate/News/Landing+Zone/commonpoint.htm>.

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## CIMdata News

### ***New Opinion Poll on the Management of Simulation/Analysis in Your Organization***

30 May 2008

A new CIMdata opinion poll is available. Click [here](#) to provide input on the following questions

Is your simulation/analysis activity across the enterprise or is it mainly found in pockets?

Do you have a corporate process for storing, retrieving, reusing, cataloging simulation/analysis data?

Where do you store simulation/analysis data?

What is your key priority regarding simulation/analysis management?

The results of these polls are tabulated as you vote. The results are completely anonymous.

If you have a suggestion for a poll you'd like to see contact us at [info@cimdata.com](mailto:info@cimdata.com).

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## Company News

### ***Accept Expands Senior Management Team With Two Appointments***

29 May 2008

Accept Software announced it has added two key new hires with deep software experience. Joining Accept are Mark Calomeni as vice president of engineering and John Racioppi as senior vice president of business development. Collectively, Calomeni and Racioppi bring 50 years of proven track record to Accept in software engineering, sales and business development.

“Our vision is to help companies gain competitive advantage by capturing the best ideas and transforming them into successful new products,” said Bryan Plug, president and CEO, Accept Software. “Both Mark and John are proven leaders with extensive industry knowledge and will be tremendous resources in helping us continue to realize our vision. We are very pleased to add them to the executive team.”

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As vice president of engineering for [Accept](#), Calomeni will have operational responsibility for overseeing all research and development activities across Accept's Innovation Management product lines. Calomeni will help define Accept's technology investments and transform them into next-generation solutions designed to drive rapid company growth. Before joining Accept, Calomeni successfully led information technology strategies and managed software delivery organizations in start-up, mid-sized and Fortune 100 companies such as Hewlett-Packard, OnDemand Inc. and Wyse Technology.

As Accept's senior vice president of business development, Racioppi will be responsible for extending the company's strategic partner and alliance ecosystem in conjunction with its overall strategy, in order to expand market reach and customer growth. Prior to Accept, Racioppi held senior management positions at various publicly-traded and start-up companies including Trend Micro, Infravio Inc., Moonlight Systems, Borland and IBM.

Both Calomeni and Racioppi have assumed their roles and report directly to Bryan Plug, president and CEO, Accept Software.

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## ***Bentley Announces Release of May 2008 Annual Report***

28 May 2008

At this first BE Conference focused on BEst Practices for Sustaining Infrastructure and dedicated to specific solutions communities, Bentley Systems released its May 2008 Annual Report. The report, which was distributed this morning to the conference's record number of more than 2,000 attendees, reviews Bentley's business priorities and highlights milestones for the past year, including:

2007 revenues reached \$450 million, reflecting 16 percent growth over 2006,

Bentley colleagues around the world now exceed 2,800,

Bentley users work in 169 countries,

Almost 80 percent of the ENR Top 50 rely on ProjectWise collaboration services,

Says CEO Greg Bentley in the report, "Bentley's May 2008 Annual Report demonstrates both continuity and change. As the world's leading provider of comprehensive software solutions for the infrastructure lifecycle, Bentley realizes the vital role infrastructure must play in sustaining our society and our environment. The imperative for sustaining infrastructure – to empower the designing and building of infrastructure assets that perform better over a long and useful life – continues to be the central driving force behind Bentley's strategies."

He continues, "At the same time, we've undertaken to increase our external visibility, with a new logo, new corporate website, and new 'professional networking' site for our user communities."

Bentley's new logo and the new Bentley.com homepage clearly reflect the company's commitment to sustaining infrastructure – "from buildings to bridges, transit to utilities, clean energy to clean water." The website features a new design, a new organization around Bentley's solutions, products, learning, and subscription services, and provides integration with BE Communities, the professional networking site that empowers infrastructure professionals to work smarter, together.

BE Communities is the most complete resource center for architects, engineers, constructors, and other

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infrastructure practitioners, managers, and executives. The new website, which is currently in final beta, brings together infrastructure professionals from around the globe to connect, communicate, and gain knowledge by interfacing with their peers in solutions communities. Among the BE Communities' tools are forums, personal blogs, a wiki, and a resource gallery to share documents, photos, videos, and other files. In addition, there's a resume center where graduates can post their qualifications and advance their careers.

To view a copy of Bentley's May 2008 Annual Report online, visit [www.bentley.com](http://www.bentley.com).

For more information about BE Conference, visit [www.bentley.com/BEconference](http://www.bentley.com/BEconference).

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## ***First Book Published on the Open Verification Methodology***

30 May 2008

Cadence Design Systems, Inc. and Mentor Graphics Corp. announced the publication of the industry's first book covering the widely adopted Open Verification Methodology (OVM). *Step-by-Step Functional Verification with SystemVerilog and OVM* is written by Dr. Sasan Iman, an industry leader in the functional verification space. Iman is a principal with SiMantis Inc. and the former director of simulation technology at Escalade Corp. whose previous books on the e verification language and low-power design methodologies have been well received by the design and verification community. The new book from Hansen Brown Publishing is being promoted by Cadence and Mentor Graphics on the OVM World Web site (<http://www.ovmworld.org>) to help the OVM community better understand and use the popular methodology.

*Step-by-Step Functional Verification with SystemVerilog and OVM* provides a complete guide and reference for adopting an effective functional verification methodology, learning the SystemVerilog language, and using SystemVerilog and the OVM library for building a verification environment for a realistic design example. With more than 500 pages of original technical content, none of which duplicates the documentation already available on OVM World, the book is unprecedented in both the breadth and depth of its information on advanced verification.

"Dr. Iman brings together all the essential elements to understand the use and application of the OVM," said Dennis Brophy, director of Strategic Business Development at [Mentor Graphics](#). "This book has everything design and verification engineers would want to know to apply the OVM to their most pressing challenges."

"The OVM is one of the most quickly and widely adopted new solutions ever for verifying complex chips," said Ted Vucurevich, CTO at [Cadence](#). "This book walks the reader through the OVM as well as the SystemVerilog language constructs upon which it is built. The breadth of this book and its pragmatic approach make it an invaluable resource for both novice and experienced verification engineers."

"This book fulfills the need for education in two critical areas: SystemVerilog verification and the OVM," said James Baldwin, Staff Verification Engineer at Qualcomm Incorporated. "The blending of these topics in a single book provides a unique and efficient source for any verification engineer who wants to rapidly learn these leading-edge concepts and put them to use with a solid understanding."

"The level of industry interest in the OVM is just amazing," said J.L. Gray, owner of the popular and influential Cool Verification blog (<http://www.coolverification.com>) "I know many companies who will eagerly welcome this book as a way to jump-start their OVM usage and their move to the most advanced

verification techniques and methodology available today."

## Availability

Step-by-Step Functional Verification with SystemVerilog and OVM is now available for ordering at either of the following sites:

<http://www.amazon.com/Step-step-Functional-Verification-SystemVerilog/dp/0981656218>

<http://www.simantis.com>

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## ***Four-Fold Increase in Educational Sites Using Flomerics EFD Software***

27 May 2008

Since Flomerics announced its EFD for Education Initiative a year ago there has been a four-fold increase in the number of sites using its Engineering Fluid Dynamics (EFD) software for teaching and research purposes. Flomerics aims to ensure that mechanical engineering students and researchers have access to the latest fluid dynamics software used in industry. Unlike traditional Computational Fluid Dynamics (CFD) simulation software, EFD operates inside the mechanical CAD tools that engineers are familiar with – without any transfer of data.

“With EFD there’s no need to learn a new user interface, no CAD export, and the meshing is fully automated, so students can focus solely on learning about fluid dynamics instead of how to use the software” said John Parry, research manager at Flomerics. “I’m delighted by how well EFD has been received by the educational community.”

Research at the University of Puerto Rico is focused on the structural behavior of canopies of gas stations under wind load. “A large number of failures of canopies during the past hurricanes Katrina and Rita in the US Gulf Coast in 2005 was observed and confirmed by observations from previous hurricanes. Our aim is to understand the influence of various parameters, including the size of parapets, location of canopies, etc. At present there is limited information on the pressure coefficients due to wind on horizontal canopies, which we will investigate using EFD” commented Luis A. Godoy, professor of civil engineering and director of the Civil Infrastructure Research Center, University of Puerto Rico at Mayaguez.

The EFD product family provides full-featured general purpose fluid flow and heat transfer analysis software combining all simulation steps, starting with reading CAD data, model definition, mesh generation, solving, post-processing, through to the generation of result presentations – all within a single software environment:

- EFD.Pro – EFD fully embedded within the Pro/ENGINEER Wildfire environment;
- EFD.V5 – EFD fully embedded CFD solution within the CATIA V5 environment; and
- EFD.Lab – a general purpose CAD-embedded fluid flow and heat transfer software.

EFD has sophisticated physical models to simulate a wide range of engineering tasks -- from creeping non-Newtonian flows to supersonic gas flows with high mach numbers. EFD is also provided with a number of detailed ready-built and documented validation cases that can be used as course material. And unlike some other traditional CFD programs, the EFD educational version has the same capabilities as the commercial version, with no restrictions on physical modeling or meshing, so it is also ideal for

research purposes.

EFD is available for immediate shipment to educational institutions. Interested readers may download their choice of free EFD online demos at

<http://www.flomerics.com/products/engineeringfluidynamics/demo.php>

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## ***IBM Opens New Global Delivery Center in Pune, India***

27 May 2008

IBM inaugurated a new Global Delivery Center (GDC) in Pune to provide clients worldwide with business consulting and application services. This is IBM Global Business Services' (GBS) fourth site in Pune, and it will expand the scope of business and technology expertise already available to clients through its globally integrated network of delivery centers.

The Pune site joins IBM's network of 20 business consulting and application services centers located in eight countries around the world. These centers, which utilize world-class tools and automation technologies from IBM Research, are established as 'centers of excellence' with professionals who are uniquely skilled and equipped to serve client needs by industry. This globally integrated approach provides the fundamental underpinning for IBM Global Business Services, enabling the company to define the professional services industry with its clients.

Due to the vast presence of original equipment manufacturers (OEMs) and automotive industry expertise in Pune, the new center serves as a strategic location for IBM's global and local automotive clients. At the Pune center, IBM offers clients a wide range of services that cover key areas such as product lifecycle management, manufacturing productivity, business consulting, human capital management, financial services, technology services, and solutions for small and midsize automotive manufacturers.

"As a globally integrated company, IBM leads in high-value creation and services delivery driven by our strategy of doing the right tasks, with the right skills, in the right places. Our new facility in Pune will further augment our globally integrated delivery network, providing clients with differentiated solutions to help them innovate and transform their businesses," said Rajesh Nambiar, Vice President and General Manager, Global Delivery, IBM India, "Pune has a rich pool of engineering talent and provides a very conducive environment for technology development. This makes a compelling case for locating our new center in Pune," he added further.

The Pune center will also provide services for clients in the Travel and Transportation, Chemical and Petroleum, and Energy and Utility industries. IBM will expand its workforce in Pune by hiring technology graduates and IT professionals with skills in IT strategy and architecture, business consulting, enterprise solutions (SAP, Siebel, and Oracle), testing, open systems, Portal, and Webmethods.

"IBM has a unique combination of automotive industry insight and talent at the Pune Global Delivery Center," said Sanjay Rishi, Global Automotive Industry Leader, IBM Global Business Services. "Through our globally integrated delivery model, clients can access IBM's automotive expertise regardless of their location, as well as replicable software and services assets developed specifically for the auto industry. No other vendor can match IBM's breadth of business-oriented and industry expertise, and portfolio of services and software assets."

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## **Differentiated capabilities: Non-English Language Skills**

IBM also announced a new Language Translation Services Center (LTSC) in the Pune facility as part of its strategy to address increasing demand from clients in Europe for non-English language skills.

The Pune center's LTSC will offer a full scope of quality language translation services for clients and professionals at other IBM global delivery centers. This includes assisting with document translations -- such as emails, web pages, contracts, etc. -- from French, German, Italian and Spanish to English, allowing for more improved and efficient communication between clients and employees around the world. More LTSC sites will open at other global delivery center sites in Asia and Europe later this year.

The LTSC is one part of IBM's three-pronged approach to streamline language translation. This approach also includes leveraging common machine language translation tools and innovative offerings from IBM Research, as well as building language skills through online and classroom training.

IBM enjoys strong global delivery and domestic business in Pune. IBM has multiple partnership arrangements with educational Institutes in the city like Symbiosis Inst of Business Management and Steinbeis, a German University, to conduct International MBA programme for its employees.

## **About IBM Global Delivery**

IBM India is at the forefront of global delivery, and has set up the complete range of its services capabilities including Consulting & Systems Integration, Infrastructure, Applications and Business Process transformation and management in India. IBM has six Global Delivery locations in India: Bangalore, Hyderabad, Kolkata, Pune, Chennai and National Capital Region (Noida and Gurgaon).

Worldwide [IBM](#) operates global delivery centers in more than three dozen countries, providing clients with business process, infrastructure, consulting and application services.

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## ***Jochen Boy Receives Bryan K. Martin Technical Excellence Award***

26 May 2008

Jochen Boy, coordinator of the [ProSTEP](#) iViP Association's CAX Implementor Forum (CAX-IF), has been presented with the Bryan K. Martin Technical Excellence Award by the international standardization consortium PDES, Inc. He received the award in recognition of the work he has done on creating implementation guidelines and developing the CAX-IF Evaluation, Statistics and Results System (CAESAR), which provides companies participating in the test rounds with faster and better evaluation. The award is a special tribute to Jochen Boy in that he is the first winner of this award who does not come from the ranks of PDES, Inc.'s member companies.

In his speech, Michael R. Jahadi, chairman of the executive board of PDES, Inc., made special mention of Jochen Boy's detailed knowledge of the STEP standard. His understanding of the nuances critical to any implementation of the STEP interface makes him the ideal person to deal with system vendors involved in this area. Jochen Boy is recognized as a leading STEP expert worldwide.

The CAX-IF is a joint working group supported by both PDES, Inc. and the ProSTEP iViP Association. Its objective is to accelerate CAX translator development and ensure that user's requirements are satisfied. Jochen Boy's involvement in the CAX-IF started as early as 1999 when he was still a student. A mathematician, Jochen Boy has been the coordinator of the CAX-IF since 2001. He is also actively involved with the ProSTEP iViP Association's CPM (Collaborative Project Management) and LOTAR

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(LONg-Term Archiving and Retrieval) project groups. The latter is concerned with the long-term archiving of digital 3D CAD and PDM data.

For Christine Frick, general manager of the ProSTEP iViP Association, the award is more proof of the value of the work carried out by the Association. “After receiving an award for our efforts involving the advancement of the STEP standard last year from the ISO (International Organization for Standardization), we are now delighted to be able to share in Jochen Boy’s pleasure at having been given this special personal award by our cooperation partner, PDES, Inc.”

## **About PDES, Inc.**

PDES, Inc. an international consortium of industry, government, and academic organizations in the USA working to accelerate the development and use of technical product data standards in order to strengthen and enhance cross-departmental and cross-enterprise collaboration in product data management. PDES, Inc. is therefore the transatlantic counterpart of the ProSTEP iViP Association. These two organizations have been working together for over 10 years.

## **About the ProSTEP iViP Association**

The ProSTEP iViP Association is an international branch-specific community comprising leading companies in the automotive and aerospace industries, system vendors and research institutes. The aim of the ProSTEP iViP Association is to find solutions for the challenges facing the manufacturing industry as a result of networked collaboration in a worldwide development network.

A concept based on a coherent, cross-organizational and cross-domain view of data, processes and systems provides a solid foundation for meeting these challenges. The Association’s five main areas of focus reflect this approach: process management, system integration, product data standardization, engineering collaboration and knowledge transfer.

The ProSTEP iViP Association is headquartered in Darmstadt, Germany, and was founded in October 1993 by 38 industrial companies and a number of system vendors as part of the German STEP initiative. Members of the ProSTEP iViP Association currently include about 200 companies and organizations from 17 nations.

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## ***New Product Development Study Revealing Interesting Trends***

27 May 2008

International TechneGroup Incorporated (ITI) and iTiD Consulting Ltd. (iTiD) are seeking participants for an on-going study to determine Best-in-Class standards for New Product Development.

Participating organizations will receive, at no cost, a report highlighting study results.

Since late 2007, ITI has been working with manufacturers throughout North America, and now Europe, to measure the manner, speed, and efficiency in which new products are developed. The study is a continuation of a program initiated in Japan by iTiD (iTiD is jointly owned by ITI and Japan's ISID and represents ITI in Japan operations).

To date, survey data has been very successful collecting data from nearly 7,000 participants representing companies around the globe and throughout industry. The goal is to continue to expand the study globally in order to build upon the current findings.

# CIMdata PLM Industry Summary

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"As study participation grows we're beginning to see some very interesting trends," explained ITI Chairman, Michael Lemon. "For example, a growing number of companies are overly dependent on the experience of an aging workforce. Often process guidelines or rules are left undocumented with regard to key decisions regarding the manner in which products are defined, designed, and developed. In many instances disorganized processes are overcome by tapping the expertise of an experienced senior engineer or project manager," continued Lemon.

"Human judgment and experience certainly have their place in new product development; but manufacturers have to take steps to ensure that this knowledge is effectively captured and doesn't retire with the individual."

## Additional Findings

Studies in Japan show that an average company grew 0.23 points from a similar study conducted in 2004. While all areas of development improved, supplier management and regulations conformance showed the largest improvement. This is a representation of the efforts to conform to new energy and eco-friendly regulations as well as managing suppliers to establish higher quality.

Also a common trait, regardless of geography, is a trend to have less rules and process definition in the early phases of new product development. Product planning and concept design rely more on individual effort than downstream processes such as detail design and evaluation.

## Participate in the Study

For more information or to participate in the study contact:

North America	Japan	Europe
Robert Farrell	Shingo Tsuda	Enrico Bergamasco
ITI	iTiD	ITI
513-576-3900	81-3-6713-5700	39-335-6263700
<a href="mailto:farrell@iti-global.com">farrell@iti-global.com</a>	<a href="mailto:tsuda.shingo@itid.co.jp">tsuda.shingo@itid.co.jp</a>	<a href="mailto:enrico.bergamasco@iti-global.com">enrico.bergamasco@iti-global.com</a>

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## *Sequence Chapter in New PFI Book Details Early Power Analysis Tips*

29 May 2008

Sequence Design's Tom Miller, Vice President and Head of R&D, Front-End Products, is one of the featured authors in a new book, "A Practical Guide to Low-Power Design – User Experience with CPF," that has been released by the Power Forward Initiative (PFI). The book can be downloaded free of charge at <http://www.powerforward.org>.

The Sequence chapter, "Early Power Analysis with CPF," details how the biggest power reductions can be achieved during architectural tradeoffs and describes multiple techniques for power analysis and optimization along with real-world examples of these approaches in action.

"PFI has gathered an impressive list of expert contributors for this book, and are once again demonstrating how the Common Power Format (CPF) helps maintain design integrity throughout the flow, while creating an environment to maximize power savings in complex designs," Miller said.

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For example, Sequence's PowerTheater can analyze designs using multiple-supply voltages and power shutoff techniques to help users make design tradeoffs at RTL resulting in more power-efficient designs, and then generate an output CPF file that captures the design intent originally specified as well as derived design intent. This CPF file will be more complete based on the automatic generation of level shifter, isolation and power switch rules as well as a complete description of the power modes that may need to be filled out, resulting in fewer errors passed to downstream tools.

In addition to Sequence, contributors to the new book include ARC, ARM, Faraday, Freescale, Fujitsu, NEC Electronics, NXP, and TSMC. PFI will be showcasing the new book in Si2's DAC Booth 1614.

## About Power Forward Initiative

The Power Forward Initiative, which has more than 25 member companies, is an industry initiative sponsored by Cadence that was formed in May 2006. It has the goal of enabling the design and production of more power-efficient electronic devices. The initiative includes companies representing a broad cross section of the design chain including system, semiconductor, foundry, IP, EDA, ASIC and design services companies. CPF was contributed by Cadence to the Si2 Low Power Coalition in December 2006 and CPF 1.0 is now available as a Si2 standard to the industry at large. The Initiative has recently published "A Practical Guide to Low-Power Design – User Experience with CPF" which is aimed at educating the broad design marketplace in utilizing advanced low-power design techniques. The Guide is available free of charge at <http://www.powerforward.org>.

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## ***Thales and Mentor Graphics Halve Time-to-Productivity for New Design Engineers***

28 May 2008

[Mentor Graphics Corporation](#) and Thales UK's engineering and process management (EPM) business have developed an integrated tool and methodology training course which halves the time-to-productivity for new engineers. Thales estimates that up to a hundred engineers could be trained this way over the coming year.

Previously, Thales' Field-Programmable Gate Array (FPGA) design engineers received three separate training courses. Mentor Graphics worked with Thales to create a training course that would integrate Hardware Description Language (HDL) theory, tool application and familiarization with Thales' in-house methodology. Whereas it typically took six months for a new recruit to undertake the three courses it should now take a maximum of three months and by combining the courses the number of training hours is reduced by 40%.

Thales UK's optronics business in Glasgow has successfully piloted the course, and it will now be extended to the Staines and Bury St Edmunds sites, then two further sites from the land and joint systems business, Crawley and Wells. It will be made available to the rest of Thales UK's sites and discussions are underway to provide the course across other European sites via Thales' France-based EPM team.

Instead of off-the-shelf tool training a combined team from Mentor Graphics Education Services and Thales' EPM business jointly deliver the bespoke course for Thales engineers. The EPM business maintains a range of 'Workbenches', workflows accessed through a Thales-developed Graphical User Interface (GUI). In the case of the FPGA Workbench this contains tools from Mentor Graphics, including the HDL Designer™ tool, the ModelSim® tool, and the Precision® Synthesis tool. The GUI guides engineers to follow Thales design rules, configuration and library management conventions and other aspects of best practice and standardisation such as DO254.

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Thales has a major graduate recruitment programme underway and the course will be an important part of the induction process, but it is also intended as a refresher for existing Thales engineers adopting FPGA Workbench. The new course will accelerate the rollout of FPGA Workbench across Thales sites, since the Workbench methodology will be imparted at the same time as tool training, generating the following benefits:

- New engineers will be productive sooner
- Better engineering resource management by enabling existing engineers to swap between projects more easily
- Encourage wider use of newer techniques, e.g. assertions, self-checking testbenches
- Cut verification time by reducing bugs through standardizing best practice
- Enhanced overall management as Workbench generates live project metrics
- Reduction in training costs

Andrew Parmley, head of electronics at Thales UK's optronics business, says: "Integrating company-specific methodology instruction with tool training is a no-brainer decision. It gets engineers productive quickly, cuts training administration and allows us to standardize best practice so that engineers can move easily between projects to put design resources where they are needed."

Adrian Buckley, General Manager of Mentor Graphics Western Europe, comments: "Our investment in this partnership approach to bespoke training makes sense because our aim is to deliver solid business outcomes for our user base. This is a transferable approach to training which we are also able to offer to other customers."

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## Events News

### ***Agilent Technologies to Feature Latest Innovations in Signal Integrity Design, RFIC Verification at 2008 Design Automation Conference***

27 May 2008

Agilent Technologies Inc. announced it will showcase its latest [signal integrity](#) design and [RFIC verification](#) innovations at the 2008 Design Automation Conference in Anaheim, Calif., June 8-13, Booth 1601. The products that will be demonstrated are geared toward analyzing and verifying today's increasingly complex high-speed interconnect and RFIC designs.

The Design Automation Conference (DAC) is the premier event for the design of electronic circuits and systems as well as electronic design automation (EDA) and silicon solutions. As a leading supplier of EDA software for high-frequency system, circuit and modeling applications, Agilent's EEs of EDA division will showcase its most recent innovations to the following design flows:

Signal integrity;

RFIC;

MMIC;

RF board/module; and

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Electronic Systems Level (ESL).

[Agilent EEsof EDA](#) offers a wide array of RF and mixed-signal design solutions that include the [Advanced Design System](#) RF EDA software, with solutions for signal integrity design and electromagnetic simulation and analysis; [GoldenGate](#) for large-scale RF/mixed-signal IC simulation and design-for-yield in the Cadence design environment; and [SystemVue](#) for RFIC system architecture.

In addition to technical meetings and presentations at the conference, Todd Cutler, senior director with Agilent's EEsof EDA division, will serve on a panel session titled "Electronics and Politics: What the Industry Needs from the Incoming U.S. Administration." Panelists will discuss the U.S. presidential candidates' proposed initiatives and changes that potentially affect the DAC community.

For more information about Agilent's participation and schedule at DAC 2008, visit [www.agilent.com/find/eesof-dac2008](http://www.agilent.com/find/eesof-dac2008). For general information about the conference, visit <http://www.dac.com/45th/index.aspx>. For information about all of Agilent's EEsof EDA products and solutions, visit [www.agilent.com/find/eesof](http://www.agilent.com/find/eesof).

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## ***Anark Showcases 3D CAD Transformation Platform at PTC/User World 2008***

28 May 2008

Anark Corporation announced it will exhibit at the annual PTC/User World, taking place in Long Beach, California. The company will demonstrate its automated 3D CAD preparation and transformation platform, Anark Core.

The Anark Core Platform reduces repetitive CAD file conversion and preparation tasks by providing automated tools that enable efficient and secure interoperability between producers and consumers of 3D CAD data for OEM-Supplier data exchange, technical illustrations, and visual simulations.

At the event, Anark will demonstrate the current platform, Anark Core version 1.1. This version provides users with automated design-feature removal capabilities (parts, holes, filets), then exports high-precision BREP and lightweight mesh file formats including CATIA V4/V5, Parasolid, STEP, NX (formerly Unigraphics), IGES, Collada, DWF, X3D, and VRML.

"With Anark Core, we're providing manufacturing companies with the power to easily and cost-effectively transform and share relevant product data internally and throughout the OEM supply chain – a repetitive task that can take hours of engineers' time every day," commented Stephen Collins, President and CEO of Anark Corporation. "We are particularly excited to demonstrate its capabilities to show attendees, as it is a powerful complement to many of PTC's leading products and solutions."

The Anark Core Platform will be on display at the annual PTC/User event at the Long Beach Convention Center, booth 419, on June 1-4, 2008. To schedule a demonstration prior to the show please contact Anark directly at [solutions@anark.com](mailto:solutions@anark.com).

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## ***Apache Design Solutions Offers Hands-On Tutorial on IP Validation for Macro and Embedded SoC at 45th Design Automation Conference***

30 May 2008

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[Apache Design Solutions](#) will host a hands-on tutorial at this years' 45th Design Automation Conference (DAC), titled "IP Validation for Macro and Embedded SoC". The tutorial gives attendees first-hand experience in running Apache's products for validating embedded macro, analyzing full-chip power including package, and generating portable power delivery network model for co-design.

**WHO:** Apache Design Solutions

**WHAT:** Hands-on-Tutorial titled "IP Validation for Macro and Embedded SoC"

**WHERE:** Design Automation Conference  
Anaheim Convention Center  
Room 213D

**WHEN:** 2:00 p.m. - 5:00 p.m., PDT  
Monday, June 9, 2008

Specifically the tutorial will cover the following:

- Block-level power analysis of a full-custom IP from Virage Logic using RedHawk-MMX
- Package model extraction using PakSi-E
- Full-chip power analysis including validated custom IP and package models using RedHawk-EV
- Chip Power Model generation using RedHawk-CPM

For more information please go to: <http://www.dac.com/events/eventdetails.aspx?id=77-140>

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## ***Apriso to Speak at Dassault Systèmes ENOVIA User Conference***

28 May 2008

**Who:** Apriso's Director of Product Management, Jean Luc Delcuvellerie, will present a case study review on how BAT (British American Tobacco) has integrated the Apriso FlexNet Manufacturing Execution System (MES) with Dassault Systemes ENOVIA MatrixOne Product Lifecycle Management (PLM). The presentation will describe the tobacco giant's business drivers for the project, its functional scope, the integration between PLM and MES and the resulting benefits, including advanced traceability, streamlined operations and improved product quality. The [Apriso](#) global manufacturing operations management solution is currently being rolled out to other BAT plants worldwide.

**What:** Dassault's European ENOVIA Customer Conference 2008 is an event for PLM users to discover the newest PLM 2.0 solutions of Dassault Systemes. This is the first European PLM Collaboration event organized since Dassault's acquisition of MatrixOne, and will offer an opportunity to meet Dassault/MatrixOne experts, customers and partners to see how ENOVIA brings PLM 2.0 solutions to the industries.

**Where:** Hilton Munich Park, Munich, Germany; see <http://www.3ds.com/news-events/eceurope/overview/>.

**When:** June 2-4, 2008

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## ***Busy Start to June for Theorem at PTC User World Event and Siemens PLM Connection Americas***

27 May 2008

Theorem Solutions faces a busy start to June, taking their key interoperability products to major user events at different locations - one in Long Beach California and the other in Orlando Florida – both during the same week.

[PTC User's World event](#) in Long Beach California and [Siemens PLM Connection Americas](#) in Orlando Florida take place at the same time and Theorem Solutions is attending both events to ensure that their customers and prospective customers are informed of the latest Theorem advances in the field of interoperability.

Theorem's Universal CATIA CADverter promises to be a popular discussion topic at both events because it provides an ability for [Pro/Engineer users](#) and [NX users](#) to translate both CATIA V4 and CATIA V5 format files with a single Theorem CADverter, offering benefits in terms of ease of use, greater flexibility and reduced costs.

[Theorem Process Manager \(TPM\)](#) is also sure to attract significant attention as a solution to the growing need to automate and manage interoperability processes including collection of data, translation, verification, distribution, archiving and production of audit trails.

[PTC Users](#) and [NX users](#) who will not be able to attend these events in Long Beach and Orlando are invited to visit [Theorem's web site](#) for information that they might otherwise have missed.

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## ***Design Automation Conference Professional Development Fund to Award More than \$170,000 this Year***

29 May 2008

For the 15th year, the Design Automation Conference (DAC) in conjunction with several sponsoring societies is awarding more than \$170,000 in development funds to students and professionals in the EDA field. More than \$2.2 million have been awarded since the inception of the Professional Development Fund. The 45th DAC will be held at the Anaheim Convention Center, Anaheim, Calif., June 8 – 13, 2008.

A range of programs, including the A. Richard Newton Graduate Scholarships, the P.O. Pistilli Advancement in Computer Science and Electrical Engineering Program, the Young Student Support Program and the University Booth Program are supported by the DAC Professional Development Fund. The Fund also supports a number of workshops that take place during DAC, such as the Workshop for Women in Design Automation.

“DAC is committed to ensuring the continued growth of the EDA industry, and we reflect that by making this annual pledge to continuing education both within academia and in professional development,” said Steve Levitan, 45th DAC Past Chair, who oversees the DAC Professional Development Fund.

Programs Receiving DAC Professional Development Funds:

- **A. Richard Newton Graduate Scholarships:** Awarded by DAC in honor of the memory of Dr. A. Richard Newton, \$24,000 in scholarships will be given to support graduate research and study in

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electronic design automation and circuit design. These scholarships are intended to support the graduate students of faculty investigators at universities establishing new programs in electronic design automation or circuit design and/or the graduate students of young faculty investigators (assistant rank, non-tenured) working in electronic design automation or circuit design. Additional information can be found at <http://www.dac.com/45th/scholar.html>.

- **P. O. Pistilli Scholarship for Advancement in Computer Science and Electrical Engineering:** Funded by DAC and directed by the Association for Computing Machinery (ACM) Special Interest Group on Design Automation (SIGDA), this scholarship is for undergraduate students. The objective of the scholarship is to increase the pool of professionals in electrical engineering and computer science from under-represented groups (Women, African American, Hispanic, American Indian, and Disabled). Scholarships of \$4,000 per year, renewable for up to five years, are awarded annually to as many as seven high school seniors from under-represented groups who have a 3.00 GPA or better (on a 4.00 scale), have demonstrated high achievement in math and science courses, have expressed a strong desire to pursue careers in electrical engineering, computer engineering, or computer science, and who have demonstrated substantial financial need. Additional information is available at <http://doc.union.edu/acsee.html>.

- **SIGDA/DAC University Booth:** Provides an opportunity for the university community to demonstrate EDA tools, design projects, and instructional materials at the Design Automation Conference. The University Booth also provides space for the presentation of EDA vendor literature and programs of interest to the university community. The University Booth offers booth space, poster areas, computers, printers, and a high-speed connection to the Internet for participating universities. The SIGDA/DAC University Booth also provides travel grants to the participating students to cover some of the transportation and lodging expense. Additional information is available at <http://www.sigda.org/programs/Ubooth/Ubooth2008/>.

- **Young Student Support Program:** Encourages advanced undergraduates and first-year graduate students (beginning first year, or finishing first and beginning second year) to join the electronic design automation profession or pursue graduate studies in this field. This program introduces students to DAC and the design automation profession through meetings, tours, and by association with a mentor who is an advanced graduate student already working in the area. This year's program expects to grant \$28,000 for students' registration fees and banquet tickets, and to help with travel expenses. The program is sponsored by DAC along with the EDA Consortium, IEEE CASS/CEDA and the ACM Special Interest Group on Design Automation (SIGDA), which also administers the Young Student Support Program. More information on this scholarship can be found at <http://vlsi.ws.binghamton.edu/~studprog08/>.

- **NSF/SIGDA/DAC Design Automation Summer School (DASS)** offers graduate students the opportunity to participate in an intensive two day course on selected areas of research and development in design automation. DASS is co-sponsored by the Association for Computing Machinery's (ACM's) Special Interest Group on Design Automation (SIGDA), DAC Professional Development Fund and the National Science Foundation. The course runs every other year at DAC. Information about the 2007 course is available at: <http://www.ece.rice.edu/~kmram/dass07/index.html>

- **Workshops at DAC:** The fund also supports and "seeds" workshops of educational and professional interest to the practicing EDA professional. This year the list of more than a dozen workshops includes the Workshop for Women in Design Automation, Maximizing Efficiency in the Development Cycle, Effective Technical Writing, Cross-layer Power and Thermal Management, Biochips to Interface and Monitor Human Biological Functions and the 4th Integrated Design Systems Workshop - OpenAccess:

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A Platform for Continuous Evolution and Innovation. More information on the workshops can be found on the [DAC Web site](#).

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## ***ESI Group Announces Keynote Speakers for EuroPAM 2008***

26 May 2008

ESI Group announced its keynote speakers and theme for its EuroPAM 2008 user conference. Designed specifically for ESI customers, EuroPAM 2008 provides a forum for experienced ESI users to share their expertise in designing better quality products and reducing time-to-market with ESI Group's simulation software products.

EuroPAM 2008 takes place on May 29-30, 2008, at the Congress Centre DIPLOMAT in Prague, Czech Republic. Attendees include experts, senior practitioners and decision-makers representing the automotive, aerospace, marine, energy, casting, and other manufacturing industries, as well as leading European academic institutions.

The theme of this year's EuroPAM conference is "Get it Right the First Time with Simulation-Based Design." As the traditional boundaries around numerical simulation continue to expand with ever-increasing improvements in computing power, revolutionary results in both design performance and economic benefits can be achieved. "ESI Group is leading the charge in the evolution from Simulation-Driven Design, which combines the virtual and the real world, to Simulation-Based Design, where end-to-end product design and manufacturing simulations are calibrated on data independent from and prior to the first real prototype," said Alain de Rouvray, Chairman and CEO of ESI Group. "ESI is ushering in a bold, new era in Virtual Product Development and Product Lifecycle Management (PLM) aimed at helping design engineers 'Get it Right the First Time'. By allowing all essential design and manufacturing decisions to be made concurrently, ahead of the hardware tooling and prototyping stage, Simulation-Based Design lets customers experience design performance improvements and economic benefits on a scale never before possible," he continued.

EuroPAM 2008 features parallel tracks in the areas of Performance Evaluation and Manufacturing Simulation. Industry leaders will present and share ideas on best practices surrounding a wide variety of applications, and ESI management and experts will present on the latest product features and benefits. This collaboration will lead to an exploration of the challenges and successes experienced by both in 'Getting it Right the First Time'.

"EuroPAM is a prestigious and renowned conference," said Michal Kalinsky, Senior CAD/FEA expert from TÜV Süd Auto CZ Ltd. "We've partnered with ESI in the area of non-linear FEM simulations for over ten years. We're looking forward to learning more about the 'next big thing' in PLM at EuroPAM 2008," he added.

Keynote speakers for EuroPAM 2008 are scheduled to include:

- Milos Safr, Head of FEM Simulations of Škoda Auto a.s., Škoda Auto
- Daniel O. Kim, Senior Project Engineer, Nissan Technical Center, North America
- André Barbe, General Manager, vehicle acoustic synthesis, Renault France

For more information about EuroPAM 2008 and to register for the event, please visit: <http://www.esi-group.com/EuroPAM2008>.

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## ***45th Design Automation Conference Shines Spotlight on Wireless Technology***

27 May 2008

The 45th Design Automation Conference (DAC) will feature a wireless theme this year, providing attendees an array of informative sessions on wireless design issues. DAC will be held June 8-13, 2008 at the Anaheim Convention Center, Anaheim Calif.

“The proliferation of wireless technology and the increasing functionality of consumer wireless devices create an ongoing challenge for chip and system designers,” said Limor Fix, 45th DAC general chair. “DAC attendees will have an opportunity to hear from leaders in wireless technology on the latest design solutions and insights on business issues as well.”

As part of the wireless theme, two keynote addresses and eight other presentations, panels and sessions will address a variety of topics geared toward wireless designers, covering an assortment of issues such as time to market, popular trends in wireless consumer products and advanced wireless design.

Justin R. Rattner, Chief Technology Officer, Intel and an Intel Senior Fellow, will deliver the opening keynote on Tuesday, June 9 on “EDA for Digital, Programmable, Multi-Radios.” Rattner will predict the impact of the industry's move toward the ubiquitous use of wireless communications and the challenges that this very high level of integration will have on design automation from architecture to manufacturing. He will address how the combination of new radio architectures and mostly digital implementations will drive a new generation of design tools, and how design techniques will evolve, by necessity, to satisfy the demands of reconfigurable hardware and software programmability.

On Wednesday, June 10, Dr. Sanjay K. Jha, Chief Operating Officer and President, Qualcomm CDMA Technologies Group will make his keynote presentation on “Challenges on Design Complexities for Advanced Wireless Silicon Systems.” Jha will address the impact the accelerating demand for 3G technology is having on the global wireless landscape. In this keynote, he will elaborate on these present and future trends and reveal how collaborative business models are changing the game in chipset design.

From Monday to Wednesday, panels and sessions will serve as the foundation for the wireless theme, kicking off Monday, June 9 with the panel “EDA Heritage Series: Maxwell’s Legacy.” The fascinating, hour long panel will provide a look into the equations and life of James Clerk Maxwell, presented by James C. Rautio of Sonnet Software, Inc.

Andrew Change, Media Tek, Inc, Atul Jain, Texas Instruments, Inc. and T.W. Williams, Synopsys, Inc. will present “Quality Versus Time to Market: The Unmentionable Tradeoff,” Tuesday, June 10 to address issues surrounding product quality in relation to time to market, turnaround time and time to results.

On Wednesday morning a panel especially designed for designers will cover “Next Generation Wireless Multimedia Devices -- Who is up for the Challenge?” will be chaired by Jan Rabaey of Univ. of California Berkeley, and featuring representatives from Qualcomm, Inc., Renesas Tech., Nokia, Texas Instruments, Inc. and Cadence Design Systems, Inc.

To conclude the wireless theme, the special session “Wireless: Business Meets Technology” will address wireless market trends in three parts, with speakers such as Jon Erensen, Gartner, Inc., Tero Rissa and Risto Savolainen of Nokia, and Matt Nowak, Riko Radojic, Christopher Chun and Jose Corleto of Qualcomm, Inc, on Wednesday, June 11 from 4:30 to 6:00 p.m.

## Registration

To register for DAC, visit <http://www.dac.com> or call 1-800-321-4573 in the U.S. to request registration materials.

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## ***Gibbs to Demonstrate Advanced 3D with HSM Enhancement at IMTS 2008***

28 May 2008

Gibbs and Associates announced that the new Advanced 3D with HSM enhancement for GibbsCAM SolidSurfacer will be demonstrated at IMTS 2008. The enhancement introduces a complete range of advanced 3-axis milling functionality with support for high-speed machining (HSM). IMTS is being held at the McCormick Place in Chicago, Illinois on September 8th – 13th, 2008.

“The new 3-axis surface machining capabilities provide more control and flexibility in toolpath generation with integral high-speed machining support,” states Bill Gibbs, founder and president of Gibbs and Associates. “We continue to expand on GibbsCAM’s world class production machining capabilities. The new functionality is keeping with GibbsCAM’s tradition of uniquely blending ease-of-use and powerful functionality, a combination that can’t be beaten in production machining.”

Advanced 3D with HSM enhancement for SolidSurfacer® includes:

- Support for 3D Material Only
- Support for more advanced tool shapes (tapered tools)
- New types of finishing processes (e.g. Surface Step-over Cut and Steep/Shallow Combination)
- Improved toolpath quality
- Options to provide toolpath that better supports High Speed Machining
- Improved use of boundaries
- Enhanced entry/exit control
- Ability to directly machine Faceted bodies, including imported STL files
- Automatic Core/Cavity detection for inside out or outside in determination
- Operation splitting for tool wear and for optimal length out of holder
- Multi-threaded for multi-CPU machines and to support batch toolpath generation
- Automatic filleting of a surface to avoid sharp concave corners

Enhancements introduced across the entire product line with the latest release of GibbsCAM will also be demonstrated at IMTS 2008.

For more information about GibbsCAM, GibbsCAM’s production machining capabilities or to see the capabilities introduced with GibbsCAM 2008, go to the GibbsCAM booth, #D-3001. Information about GibbsCAM is also available at <http://www.GibbsCAM.com>. For information about CimatronE, Cimatron’s solution for tooling manufacturing, please visit the Cimatron booth at IMTS, D-3037, or go to <http://www.Cimatron.com>.

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## ***GibbsCAM Interoperability with Pro/ENGINEER Wildfire Showcased at PTC/USER World Event 2008***

30 May 2008

[Gibbs and Associates](#), developer of GibbsCAM®, a [Cimatron](#) company, announced that they will be demonstrating the latest level of interoperability between GibbsCAM and Pro/ENGINEER® Wildfire™ at the PTC/USER World Event 2008 held June 1st - 4th at the Long Beach Convention Center in Long Beach, CA. GibbsCAM's interoperability capability is built using PTC's Granite technology ensuring the highest degree of data access.

“GibbsCAM's ability to seamlessly read and machine models from Pro/ENGINEER Wildfire,” states Bill Gibbs, president and founder of Gibbs and Associates, “offers the manufacturing engineer, who is working in a facility which uses Pro/ENGINEER, a CNC programming solution that is easy to use yet extremely powerful. The Granite technology, on which this interface is built, allows GibbsCAM complete access to the Pro/ENGINEER Wildfire data.”

Reading in a native Pro/ENGINEER Wildfire file directly into GibbsCAM is extremely straight forward as it's a file type supported by GibbsCAM's File Open dialog. GibbsCAM is able to directly read both Pro/ENGINEER Wildfire piece parts and assemblies. Once read into GibbsCAM, processes can be applied to machine the part. GibbsCAM's full associativity between part geometry, processes and toolpath supports easy updates to respond to incremental changes to either part geometry or process parameters.

This is the fourth year that Gibbs and Associates, a Silver partner in PTC's Partner Advantage Program, has participated at PTC/USER as a Bronze Premium Partner. Visitors to PTC/USER can see demonstrations of GibbsCAM at Booth #MP8.

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## ***Join Arena Solutions June 5th for Summer '08 Arena PLM Online Launch Event***

May 2008

New Arena PLM Summer '08 Release Expands Core Functionality to Facilitate Better Designs, Decisions, and Products

**WHAT:** Live online launch event gives both experienced Arena PLM customers and brand-new users an introduction to the next release of Arena Solutions' on-demand Arena PLM solution.

**WHEN:** Thursday, June 5, 2008

**TIME:** 10 a.m. – 11 a.m., PDT (1:00 p.m. – 2:00 p.m., EDT)

10:00 a.m. – 10:15 a.m. — CEO Keynote Address

10:15 a.m. – 10:45 a.m. — Product Demonstration

10:45 a.m. – 11:00 a.m. — Q & A/Closing Remarks

**LOCATION:** Online Webcast

[Follow this link to register for the online launch of Arena PLM Summer '08](#) and login details will be emailed.

**SPEAKERS:**

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Craig Livingston, chief executive officer

Eric Larkin, chief technology officer

**FOR MORE INFORMATION:** Contact Arena Solutions, (866) 937-1438

## **DETAILS:**

With the Summer '08 release, Arena PLM expands its change management functionality to enable companies to close the loop on the change process from the earliest stages of design to the final phases of production. Other new features improve visibility, control and customization.

Attend the June 5 launch event to learn more about:

The new Arena PLM EDA Adapter, which lets users push the latest approved component data directly to their electronic design environment

Change implementation tracking, which offers greater visibility into the impact of a proposed change and allows the manufacturing team—including suppliers—to document the steps taken to address an approved change

Access control improvements that include the ability to mark files and implementation notes as private

New customization options like the ability for users to create multiple custom BOM views and see exactly the information he or she needs

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## ***Mentor Graphics Chairman and CEO to Moderate 6th Annual ESL Symposium Panel at DAC 2008***

28 May 2008

Mentor Graphics Corporation announced that Walden C. Rhines, chairman and CEO, will moderate the 6th annual Electronic System Level (ESL) Symposium panel during the Design Automation Conference (DAC) 2008 in Anaheim, California. For more information and to register, please visit:

<http://www.mentor.com/go/eslpanel>

**What:** 6th Annual ESL Symposium panel, “Finding the Common Ground on Successful ESL Methodologies: Views from ‘Real World’ Users.” A panel of expert users of ESL tools and methodologies will share their commonalities, insights, and experiences. Lunch is included at this free panel event, but registration is required.

**Who:** Walden C. Rhines, Mentor Graphics chairman and CEO will moderate the panel with the following participants:

- o STMicroelectronics – Nitin Chawla
- o STARC – Kas Yoshinaga of Oki Semiconductor
- o Rambus – Prakash Rashinkar
- o Thales – Bernard Candaele
- o Qualcomm – Viraphol Chaiyakul

**Where:** Anaheim Convention Center, Anaheim, California, Ballroom E at DAC 2008

**When:** Presentation takes place Wednesday, June 11 at noon, Pacific

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## ***RuleStream to Bring Leading Edge Technology to PTC/USER World Event***

27 May 2008

RuleStream Corporation will spotlight its enterprise-class design automation software from June 1-4th at the PTC/User World Event 2008 in Long Beach, CA.

As a Gold Tier PTC Partner, RuleStream will exhibit its integrated solution in booth #210. RuleStream's flexible, scalable Standards Based Engineering (SBE) solutions help capture, manage and reuse engineering knowledge to automate sales and engineering processes across the enterprise. RuleStream's deep, bi-directional integration with Pro/ENGINEER provides rapid engineering at the time of quotation, dynamically generating new full-blown 3D assemblies for better quoting and costing, as well as downstream engineering deliverables such as work instructions, routings and MBOMs.

RuleStream's solution for Engineer to Order (ETO) manufacturers leverages existing PTC products and investments which:

- Extends the product lifecycle management (PLM) offering of PTC to encompass ETO requirements
- Integrates with Windchill to assure a seamless enterprise environment
- Provides Pro/ENGINEER users rapid engineering of new product variants at the time of quotation
- Makes the vast capabilities of knowledge management readily available to PTC engineering users

[RuleStream](#) will be highlighted in the conference session entitled "Automating Pro/ENGINEER Models to Meet Custom Product Requirements" on Tuesday, June 3rd at 10:45am in room 101A. Join us to see real examples of how RuleStream can help dynamically generate full-blown 3D models to meet custom product requirements.

"RuleStream's solutions capture, manage, and reuse product knowledge, significantly extending the value of PTC's Pro/ENGINEER, which helps ETO manufacturers respond more quickly, efficiently, and accurately to customer-specific orders," said Robert Nierman, chief executive officer of RuleStream. "Our software provides the tools manufacturers need to create a truly sustainable process for continuous improvement and innovation. We are excited to bring RuleStream's unmatched production configuration and engineering automation to one of the biggest events of the year."

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## ***Si2 Announces Member Demonstrations at the Design Automation Conference***

28 May 2008

Seven companies/organizations will be demonstrating their Si2 project-based products and solutions in the Silicon Integration Initiative (Si2) Booth #1614 at the Design Automation Conference on June 9-12, in Anaheim, CA.

**AnaGlobe Technology:** will demonstrate their OpenAccess-based integrated solution, customizable framework, supporting three scripting languages: TCL, Perl, and Python. It includes a high productivity layout platform compatible with Virtuoso 6.x as well as Calibre link. An OpenAccess-based layout editor assists APR tools in post-layout. It also includes a constraint-based OA Pcell mechanism that is customized for test chip structure generator and automatic panel layout generator. It is a production

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solution proven by IC foundries, IC design houses, and flat panel makers.

**Analog Rails:** will demonstrate analog IC design semi-automation with a fully featured schematic and mixed signal simulation, optimization, and layout environment. Analog layouts can be generated on the fly providing instantaneous parasitic backannotation. It contains a layout editor and automatic router. A single database allows crossprobing between waveform, schematic, and layout editor. It is also DRC/LVS correct by construction. Rulers not needed. Devices automatically snap and align, taking into account design rules and connectivity from the schematic. The simulation environment handles multiple testbenches and simulators, and Veritools digital simulation diagnostics.

**Artwork Conversion:** will demonstrate two Open Access based products – Qckvu3, a high performance layout viewer that can be used as-is or customized to work with other Open Access applications and oaBool, a plug-in boolean engine which performs XOR, AND, MINUS, OR and Sizing operations. The boolean plug-in inherits its class from OA's Region Query making it very easy to add to any application.

**Cadence Design Systems:** will discuss their products and industry ecosystem support of CPF 1.0. Cadence's comprehensive low-power solution leverages CPF 1.0 to provide a holistic and scalable solution to the electronics industry, allowing designers to incorporate advanced low-power design techniques in their production designs under aggressive design schedules. To demonstrate the broad support for CPF in the ecosystem, Cadence will preview highlights of user experiences using CPF-based design flows that are published in the Power Forward Initiative's (<http://www.powerforward.org>) on-line Practical Guide to Low-Power Design with CPF – User Experience with CPF.

**Fenix-DA:** will demonstrate its Crossfire product which offers cross-format consistency and integrity checking for cell libraries and IP blocks. Using Crossfire, designers can avoid view-inconsistencies like missing timing arcs or LEF vs. GDS differences. Crossfire supports the design-databases OpenAccess, Cadence CDB and Synopsys' MilkyWay, description formats like Liberty, Verilog and VHDL and it can read Library and IP datasheets.

**OpenAccess Coalition:** will demonstrate two Si2-developed debugging tools: Si2oaD will display all OpenAccess Objects, their attributes, and associations, exporting readable XML or active HTML in a GUI. Si2oaD enables visualization of persistent data on disk or in a run-time model. It can be injected into an executable and called from inside a C++ debugger to penetrate class data-hiding with automated model traversal. Si2dff is a new, customizable engine that compares individual Objects or complete Libs reporting the attribute and Object differences between them. Using unique "dff signatures", Si2dff performs a canonical traversal of all attributes and Objects in two contexts, generating "dff events" for which any number of event handling "reporter" modules can be registered.

**Open Modeling Coalition:** will demonstrate the Open Model Calculation Interface (OMCI) library. This library will enable a consistent view and use of various models across applications, thus allowing tools in multi-vendor design flows to better correlate with each other. The library leverages the Open Access API and data model and is extensible to allow for a variety of design models such as IEEE 1481 DPCM, Liberty, ECSM and statistical ECSM among others. Detailed descriptions of the architecture, API and the OMCI roadmap will also be available.

Pulsic Limited: will be showcasing the latest version of its flagship product line - Unity - which shall be running directly on Open Access (OA). The demonstration will cover fully integrated hierarchical floorplanning (including critical/global signal management and block & pin placement) with block level implementation (including placement, routing & editing).

The Annual Si2 Member/Guest Meeting will be held on June 9, 2008 from 6-8:00 PM in the Anaheim

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Convention Center, Room #304B, Anaheim, CA. You do not have to be a member to attend the meeting. All attendees should notify Si2 if they are coming at: <http://www.si2.org/?page=3>

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## ***Simulation Software at Unmanned Air, Land and Marine Vehicle Conference Promotes Innovation, Faster Design Cycles, and Optimized Solutions***

27 May 2008

Noran Engineering, Inc. (NEi) will demonstrate new software at the Association for Unmanned Vehicle Systems International Conference (AUVSI) in San Diego, CA June 10- 12. NEi's software portfolio allows engineers to virtually test air, land, and maritime vehicles and components for mechanical stress, deformation, vibration, thermal and other technical parameters at the design stage before prototypes or parts are fabricated. Engineering simulation benefits the overall design process by giving developers the tools to explore design alternatives, identify problems, optimize component function, and save time and resources in prototypes and test.

Examples of new virtual test technology from NEi includes Automated Impact Analysis (AIA™), composite material modeling and Progressive Ply Failure Analysis (PPFA™), Automated Surface Contact Generation (ASCG™), and Design Optimization. AIA is used for a variety of impact scenarios from military projectiles to drop testing of electronics like cellphones and laptops. PPFA provides insight into composite damage mechanisms so engineers can get the maximum benefit from this class of material. Composites are a frequent material choice in unmanned systems because of their exceptional performance characteristics like high strength to weight ratio and stealth. ASCG makes realistic simulation of contact in assemblies practical and affordable because it eliminates the tedious time consuming aspects in this task. Design Optimization finds solutions to problems that have competing parameters, for example, structures that must be both strong and light.

NEi is at Booth # 135 and invites attendees with an interest in virtual test software to sign up prior to the show at <http://www.NENastran.com/AUVSI2008> so material may be prepared that can address your specific application.

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## Financial News

### ***Ansoft Corporation Revenue Increases Nearly 20%***

29 May 2008

Ansoft Corporation announced financial results for its fourth quarter of fiscal 2008 ended April 30, 2008.

#### **Information Dissemination**

PLEASE NOTE: In light of the press release issued on March 31, 2008, announcing the signing of a definitive agreement relating to the acquisition of Ansoft Corporation by ANSYS Inc., there will be no conference call to discuss the results of the fourth fiscal quarter of 2008.

#### **Results for the Fourth Quarter and Fiscal Year 2008**

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Revenue for the fourth quarter totaled \$33.9 million, an increase of 19%, compared to \$28.6 million reported in the previous fiscal year's fourth quarter.

On a non-GAAP basis, net income for the fourth quarter was \$10.7 million, or \$0.43 per diluted share, compared to net income of \$8.7 million, or \$0.33 per diluted share in the previous fiscal year's fourth quarter, representing a 30% increase in diluted earnings per share. Non-GAAP net income excludes merger-related expenses, net of taxes, stock-based compensation, net of taxes, and amortization of intangibles from acquisitions, net of taxes. See "Discussion of Non-GAAP Financial Measures" below for further information.

On a GAAP basis, net income for the fourth quarter was \$8.5 million, or \$0.34 per diluted share, compared to GAAP net income of \$7.9 million, or \$0.30 per diluted share in the previous fiscal year's fourth quarter. Current quarter results include \$1.7 million, or \$0.07 per diluted share in merger-related expenses.

"We experienced revenue growth in both domestic and international markets and in both product lines," said Nicholas Csendes, Ansoft's president and CEO. "We are pleased to have had such a strong finish to our fiscal year with nearly 20% growth in revenue during the fourth quarter."

Csendes noted, "On March 31, 2008, we announced that Ansoft and ANSYS had signed a definitive agreement for ANSYS to acquire Ansoft. We believe there are strong synergies between the two companies and are very excited about the combination. In light of the proposed transaction, we will not be providing guidance for future periods."

Revenue for the fiscal year totaled \$103.4 million, an increase of 16% compared to \$89.1 million reported in the previous fiscal year.

On a non-GAAP basis, net income for the fiscal year was \$27.6 million, or \$1.09 per diluted share, compared to net income of \$23.1 million, or \$0.88 per diluted share in the previous fiscal year, representing a 24% increase in diluted earnings per share. Results for the previous fiscal year include a tax benefit of \$0.9 million, or \$0.03 per diluted share, for the U.S. Research and Development Tax Credit enacted retroactive by Congress in December 2006 that relates to credits earned in fiscal year 2006.

On a GAAP basis, net income for the fiscal year was \$24.1 million, or \$0.95 per diluted share, compared to GAAP net income of \$20.2 million, or \$0.77 per diluted share in the previous fiscal year. Current year results include \$1.7 million, or \$0.07 per diluted share in merger-related expenses. Results for the previous fiscal year include a tax benefit of \$0.9 million, or \$0.03 per diluted share for the U.S. Research and Development Tax Credit enacted retroactive by Congress in December 2006 that relates to credits earned in fiscal year 2006.

## **Discussion of Non-GAAP Financial Measures**

We utilize a number of different financial measures, both GAAP and non-GAAP, in analyzing and assessing the overall performance of our business and in making operating decisions. We consider the use of the non-GAAP measures to be helpful in assessing the performance of the continuing operations of our business. By continuing operations, we mean the ongoing revenue and expenses of the business excluding certain items that render comparisons with prior periods or analysis of ongoing operating trends more difficult, such as expenses not directly related to the actual cash costs of development, sale, delivery or support of our products and services.

Consistent with this approach, we believe that providing the non-GAAP information to investors, in

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addition to the GAAP presentation, allows investors to view our financial results in the way management views the operating results. We further believe that providing this information allows investors to not only better understand our financial performance but, more importantly, to evaluate the efficacy of the methodology and information used by management to evaluate and measure such performance.

While management uses these non-GAAP financial measures as a tool to enhance their understanding of certain aspects of our financial performance, management does not consider these measures to be a substitute for, or superior to, the information provided by GAAP financial measures. Further, investors are cautioned that there are material limitations associated with the use of non-GAAP financial measures as an analytical tool. In particular, some of the adjustments to our GAAP financial measures reflect the inclusion or exclusion of items that are recurring and will be reflected in our financial results for the foreseeable future.

In addition, other companies, including other companies in our industry, may calculate non-GAAP net income (loss) differently than we do, limiting its usefulness as a comparative tool. Management compensates for these limitations by providing specific information regarding the GAAP amounts included and excluded from the non-GAAP financial measures. In addition, management evaluates the non-GAAP financial measures together with the most directly comparable GAAP financial information.

A detailed explanation of each of the adjustments to the non-GAAP financial measures we use is described below. This press release also contains a reconciliation of each of these non-GAAP financial measures to its most comparable GAAP financial measure.

**Amortization of intangibles from acquisitions and its related tax impact.** We incur amortization expense related to various acquisitions we have made over the years. Management excludes these expenses and their related tax impact for the purpose of calculating non-GAAP net income and non-GAAP diluted earnings per share when it evaluates our continuing operational performance because these costs are fixed at the time of an acquisition, are then amortized over a period of several years after the acquisition and generally cannot be changed or influenced by management after the acquisition. Accordingly, management does not consider these expenses for purposes of evaluating our performance during the applicable time period after the acquisition, and it excludes such expenses when making decisions to allocate resources. We believe that providing this non-GAAP financial measure is useful to investors because it allows investors to evaluate the effectiveness of the methodology and information used by management in its financial and operational decision-making.

**Stock-based compensation expense and its related tax impact.** We incur expense related to stock-based compensation included in our GAAP presentation of sales and marketing expense, research and development expense and general and administrative expense. Although stock-based compensation is an expense and viewed as a form of compensation, management excludes these expenses for the purpose of calculating non-GAAP net income and non-GAAP diluted earnings per share when it evaluates our continuing operational performance. Specifically, we exclude stock-based compensation during our annual budgeting process. The annual budgeting process is the primary mechanism whereby we allocate resources to various initiatives and operational requirements. Additionally, the annual review by the board of directors during which it compares our historical business model and profitability as it relates to the planned business model and profitability for the forthcoming year excludes the impact of stock-based compensation. We believe that providing this non-GAAP financial measure is useful to investors because it allows investors to evaluate our operating results and the effectiveness of the methodology used by management to review our operating results. Further, we believe that excluding stock-based

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compensation expense allows for a more accurate comparison of our financial results to previous periods during which our equity compensation programs were not required to be reflected in our statements of operations.

Merger-related expenses and its related tax impact. We incurred professional fees related to our pending merger with ANSYS, Inc during the fourth quarter of the current fiscal year. Management excluded these expenses and their related tax impact for the purpose of calculating non-GAAP net income and non-GAAP diluted earnings per share when it evaluated our continuing operational performance because these costs are specific to the pending merger and not our ongoing operations. We believe that providing this non-GAAP financial measure is useful to investors because it allows investors to evaluate the effectiveness of the methodology and information used by management in its financial and operational decision-making.

Pursuant to the requirements of Regulation G, we have provided a reconciliation of the non-GAAP financial measures to the most directly comparable GAAP financial measures (see full [press release](#) for details)

## **Important Additional Information to be Filed with the SEC**

In connection with the proposed acquisition of Ansoft by ANSYS, Inc., ANSYS filed with the SEC a registration statement on Form S-4 (Registration No. 333-150435), which includes a preliminary prospectus/proxy statement of ANSYS and Ansoft and other relevant materials in connection with the proposed transaction. This material is not a substitute for the prospectus/proxy statement regarding the proposed transaction. Investors and security holders of ANSYS and Ansoft are urged to read the preliminary prospectus/proxy statement and the other relevant material and the final prospectus/proxy statement when they become available because they contain important information about ANSYS, Ansoft and the proposed transaction. The prospectus/proxy statement and other relevant materials, and any and all documents filed by ANSYS or Ansoft with the SEC, may be obtained free of charge at the SEC's web site at [www.sec.gov](http://www.sec.gov). In addition, investors and security holders may obtain free copies of the documents filed with the SEC by ANSYS by directing a written request to ANSYS, Inc., Southpointe, 275 Technology Drive, Canonsburg, Pennsylvania 15317, Attention: Investor Relations. Investors and security holders may obtain free copies of the documents filed with the SEC by Ansoft by directing a written request to Ansoft Corporation, 225 West Station Square Drive, Suite 200, Pittsburgh, PA 15219, Attention: Investor Relations. **INVESTORS AND SECURITY HOLDERS ARE URGED TO READ THE PROSPECTUS/PROXY STATEMENT AND THE OTHER RELEVANT MATERIALS BEFORE MAKING ANY VOTING OR INVESTMENT DECISION WITH RESPECT TO THE PROPOSED TRANSACTION.**

This communication shall not constitute an offer to sell or the solicitation of an offer to sell or the solicitation of an offer to buy any securities, nor shall there be any sale of securities in any jurisdiction in which such offer, solicitation or sale would be unlawful prior to registration or qualification under the securities laws of such jurisdiction. No offering of securities shall be made except by means of a prospectus meeting the requirements of Section 10 of the Securities Act of 1933, as amended.

## **Participants in the Solicitation**

ANSYS, Ansoft and their respective executive officers, directors and trustees may be deemed to be participants in the solicitation of proxies from the security holders of Ansoft in connection with the proposed transaction. Information about the executive officers and directors of ANSYS and their ownership of ANSYS common stock is set forth in the proxy statement for ANSYS' 2008 Annual

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Meeting of Stockholders, which was filed with the SEC on April 3, 2008. Information about the executive officers and directors of Ansoft and their ownership of Ansoft common stock is set forth in the proxy statement for Ansoft's 2007 Annual Meeting of Stockholders, which was filed with the SEC on July 26, 2007. Investors and security holders may obtain additional information regarding the direct and indirect interests of ANSYS, Ansoft and their respective executive officers, directors and trustees in the proposed transaction by reading the prospectus/proxy statement referred to above.

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## ***Arena Solutions Announces 46 Percent Growth in Sales of Arena PLM for First Quarter 2008***

28 May 2008

Arena Solutions announced a gain of 46 percent in sales of Arena PLM for its first quarter, ending March 31, 2008. A new customer went live on Arena PLM every three business days. In addition, the first quarter marks the company's best ever for customer retention and increased investment. At a net retention rate of 114 percent, the vast majority of existing customers renewed their Arena PLM subscriptions and many increased their investments in the solution too. This demonstrates not only the scalability of Arena PLM, but also the support the software provides in helping companies succeed and grow.

Arena PLM's modern web-based architecture enables customers to deploy the solution faster, easier and at a lower cost than traditional client/server applications, and as a result, see an instant return on their investment. During Q1 2008, nearly 85 percent of the new customers that went live on Arena PLM completed the entire implementation process -- from purchase date to deployment to go-live -- in less than 12 weeks. More than half of that group implemented Arena PLM in eight weeks or less.

Arena further expanded its international footprint, with a growing number of manufacturers from around the world opting for Arena PLM to help them maintain tight control over their product record. International customers this quarter -- from countries as geographically dispersed as Australia, Germany, Israel and Pakistan -- took advantage of the simplicity involved with purchasing Arena PLM. As a web-based software-as-a-service (SaaS) application, Arena PLM is easily purchased over the Internet, with workshops available online as part of a unique try-and-buy program that allows potential customers to try out Arena PLM with their own data before ever purchasing the product.

Companies that are challenging the status quo and bringing innovation and change to their industries continue to select Arena PLM to streamline business processes and manage manufacturing outsourcing without the high costs, complexities and limitations associated with traditional client/server PLM applications. Arena PLM is a strategic tool for each of them, helping them succeed and grow. A sampling of the companies that have recently adopted Arena PLM include ZENN Motor Company, SemEquip, Inc., First Act Inc., Nordic Windpower LLC, and Novare Surgical Systems Inc.

Other significant highlights of Q1 2008:

- [Arena](#) named former Agile SME Solutions division vice president and general manager Craig Livingston as the company's new chief executive officer. Livingston brings to Arena his significant experience in developing, marketing, distributing, and supporting PLM solutions focused on the needs of small and mid-sized manufacturers.
- Arena surpassed its 99.5 percent service level agreement for the eighth consecutive quarter, with 99.9 percent uptime in Q1 2008 and a continued commitment to building and delivering the most secure,

reliable PLM solution available.

"Companies using Arena PLM are experiencing a great deal of success in running and growing their businesses. Our high retention and upsell rates speak directly to the value the software provides and how well it has been able to scale with our customers as they grow. Arena PLM addresses our customers' most compelling needs, including product change management, manufacturing outsourcing, supplier collaboration and more," said Craig Livingston, chief executive officer of Arena Solutions. "And those needs are universal. With an easy-to-buy, easy-to-deploy, easy-to-use solution like on-demand Arena PLM, companies anywhere in the world can enjoy the benefits of PLM."

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## ***AVEVA Group Results for the Financial Year ended 31 March 2008***

27 May 2008

AVEVA Group plc announces its audited results for the year ended 31 March 2008.

### **Highlights**

Another year of record levels of revenue and profit

Revenue up by 34% to £127.6 million (2007 - £94.9 million)

Recurring revenue up 25% to £66.1 million (2007 - £52.7 million)

Adjusted profit before tax, amortisation, share-based payments and goodwill adjustment up 70% to £47.9 million (2007 - £28.1 million)

Adjusted earnings per share up 74% to 55.22p (2007 - 31.71p)

Profit before tax up 83% to £45.0 million (2007 - £24.6 million)

Basic earnings per share up 91% to 50.80p (2007 - 26.59p)

Investment in Research and Development up 21% to £21.3 million (2007 - £17.6 million)

Net cash at the year end of £82.8 million (2007 - £41.3 million)

Increased final dividend of 5.0p (2007 - 2.94p) bringing the full year dividend to 6.65p (2007 - 4.18p) - an increase of 59%

Nick Prest, Chairman, commented:

"AVEVA's success for many years has been built upon our world leading technologies developed specifically for the design of complex engineering projects, our excellent customer relationships and the quality of staff we employ.

"Looking ahead the Board believes the outlook for the current year remains very positive for the business. The Oil and Gas, Power and Marine industries remain buoyant, driven by high commodity prices and strong underlying end-user demand. The strength of our technology and market position combined with our focus on serving the needs of our target industries today and in the future augur well for the continued success of our business."

To view complete results, click here [Financial Results](#)

To view the presentation, click here [Financial Results Presentation](#)

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## Implementation Investments

### ***agron, inc. Implements PTC® FlexPLM™ to Increase Information Reuse and Improve Operational Efficiency***

27 May 2008

PTC announced that agron, inc., the accessory licensee for [adidas](#) America, is using PTC's Retail, Footwear and Apparel PLM solution to manage its accessory product lines from design concept through product development and catalog creation. Using PTC's FlexPLM, agron is able to improve access to product information across its multiple business groups in order to increase information reuse and improve operational efficiency.

Agron's legacy system had multiple distributed databases that resulted in data inconsistencies and inefficiencies. To improve overall accuracy and product development efficiencies, agron opted for a consolidation strategy that would control and manage all the information used by its product teams, catalog creation teams and ERP system in a single system to drive improved product development and marketing activities. Additionally, the desired solution would be rich with out-of-the-box capabilities and not require extensive configuration to achieve the desired results.

Within 16 weeks of the purchase of the PTC software, agron began using FlexPLM as its scalable, web-based PLM platform that was accessible by various departments and business groups to manage agron's entire accessory product development process, beginning with line planning and ending with providing product data and imagery for its product catalogs. In addition to using FlexPLM to manage the graphics placed in its catalogs, agron is also taking advantage of FlexPLM's line planning capability to support line plan viewing and to be the gathering place for all its contributors in a line development effort. The agron accessory products are represented within line sheets and have detailed information that includes images, fabrics and development status. Ultimately, the new system from PTC enables agron to streamline product development and reduce the time to create and publish product catalogs.

"Once we concluded that our current system could not support our growth, our executive management empowered a team to research PLM options and make a recommendation," said Jeff Goldman, project manager, agron, inc. "We chose FlexPLM from PTC because it met our needs with the least amount of customization and because it is also extremely flexible and can scale to meet our future PLM needs."

Reducing the time from concept to consumer is a strategic imperative of many organizations in the retail, footwear and apparel industries. FlexPLM delivers an intuitive set of automated capabilities in a unified user environment to streamline communication, track milestones, pre-empt performance bottlenecks and provide instant visibility of all product development steps. It enables organizations like agron to accelerate product development by allowing internal and external participants to work together in a collaborative environment.

"Agron's PLM strategy covers every stage of the product lifecycle, including line planning, materials management and product catalog production, which is why they needed a powerful PLM solution that could be implemented quickly and is capable of supporting all these various requirements," said Kathleen Mitford, vice president, vertical market strategy for retail, footwear and apparel, PTC. "FlexPLM was developed to deliver to organizations like agron a true enterprise-level solution that addresses the need for effective collaborative product lifecycle management."

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## ***Autodesk Announces Sound Devices as Inventor of the Month for May 2008***

28 May 2008

Autodesk, Inc. announced that Sound Devices, LLC a leading manufacturer of professional audio recording and mixing equipment, has been named as the Autodesk Inventor of the Month for May 2008. [Sound Devices](#) relied on Autodesk Inventor software to develop the 788T, an innovative 8-track digital audio recorder. This compact device allows audio professionals to capture and process sound more effectively in a variety of "on location" situations -- from documentary films and reality television programs, to music events and sports broadcasts -- which ultimately provides a more satisfactory audio experience for listeners and viewers.

The Inventor of the Month program recognizes the most innovative design and engineering advancements made by the extensive community using Autodesk Inventor software .

To design the 788T, Sound Devices had to figure out a way to fit eight full-featured microphone inputs and eight tracks of recording into a chassis weighing less than four pounds and roughly the size of a hardcover book.

Digital prototypes created with products from the Inventor family of products proved invaluable in helping the company check for interferences between various components and explore design variations before anything was actually built.

"Using Inventor, we were able to digitally prototype the 788T nearly a dozen times before we cut a single sheet of metal," said Jason McDonald, Design Engineer for Sound Devices. Jon Tatoes, Managing Director of Sound Devices added, "The flexibility that Digital Prototyping provides does more than help us avoid the time and costs associated with physical prototypes -- it also allows us to incorporate customer feedback into the decision-making process at a very early stage, which helps ensure we're creating products that truly meet our customers' needs."

McDonald estimates that Digital Prototyping helped the company reduce its product development time by nearly 50 percent, as the company went from initial design sketches of the 788T to full production within a period of just six months. Specific Inventor functionality such as the Sheet Metal Design feature -- which provides an accurate 3D model of sheet metal folding-- helped the company to explore different product configurations until it was able to come up with a portable audio recorder that delivered outstanding performance without a size, weight or cost penalty.

"Manufacturers of every type are feeling the pressure of compressed product development cycles," said Robert "Buzz" Kross, senior vice president of Autodesk Manufacturing Solutions. "Successful companies like Sound Devices have realized that Digital Prototyping can help them thrive in this environment by increasing innovation, even as they reduce the time to market for their pioneering new products. It is a pleasure to name Sound Devices as our Inventor of the Month for May."

About the Autodesk Inventor of the Month Program

Each month, Autodesk selects an Inventor of the Month from the more than 700,000 users of Autodesk Inventor software. Winners are chosen for engineering excellence and groundbreaking innovation. For more information on Autodesk Inventor of the Month, contact us at [IOM@autodesk.com](mailto:IOM@autodesk.com).

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## *Bentley Announces Winners of 2008 BE Awards of Excellence*

30 May 2008

Bentley Systems, Incorporated announced the winners of the 2008 BE Awards of Excellence, which honor the extraordinary work of Bentley users improving and sustaining the world's infrastructure. Seventeen projects in the professional portion of the program received BE Awards during the BE Conference in Baltimore. These projects set benchmarks and showcase the imagination and technical mastery of the organizations that created them. In addition, for only the second time, a BE Award was presented for Lifetime Achievement. In the academic portion of the program, BE Awards went to the Educator of the Year and the top four student designs.

The independent panels of BE Awards jurors, which included accomplished Bentley users and distinguished industry experts, selected the winning projects from more than 250 nominations.

"This is a year of change for [BE Conference](#)," said CEO Greg Bentley. "For the first time, its theme is BEst Practices for Sustaining Infrastructure, and its session tracks are dedicated to specific solutions communities, allowing our users to tap directly into the best practices and technology knowledge they need for current and upcoming projects.

"But there's also a constant at BE Conference 2008 – our BE Awards of Excellence program and its focus on the extraordinary, innovative work of our users. These projects, along with the imagination of our users and their dedication to improving the quality of life for all, are the very reason for our conference and for what Bentley does. I heartily congratulate the winners and acknowledge the exceptional quality of all of the nominations."

Bentley will post highlights of this year's winning projects on our website and include detailed descriptions of all nominated projects in the next edition of its "Year in Infrastructure" yearbook. The print versions of this publication have a circulation of more than 100,000, and the digital versions are searchable across more than 800 world-class projects recognized in the BE Awards program since 2004. To review the last four yearbooks, visit <http://www.bentley.com/projectsearch>.

The 2008 BE Awards of Excellence winners are as follows:

### **Professional Awards**

- Attaining Return on Innovation: Camp Dresser and McKee, Inc. – CDM Arbennie Pritchett Water Reclamation Facility
- Communicating Through Visualization: Lochner, Inc. – Fort Drum Connector (I-81 to US 11)
- Connecting Project Teams: HDR Engineering, Inc. – Southwest Parkway Interchange
- Developing the Infrastructure Workforce: Gresham, Smith and Partners
- Innovation in Cadastre and Land Development: ISKI Genel Mudurlugu – Asset Management Information System of Istanbul Water and Sewerage Administration
- Innovation in Commercial or Residential Building: Arup – Melbourne Rectangular Stadium
- Innovation in Industrial Facilities: GHAFARI Associates, LLC and General Motors Corporation – Enabling Integrated Project Delivery Using ProjectWise and BIM
- Innovation in Metals and Mining: Hatch Associates Limited – Pueblo Viejo
- Innovation in Military Installations, Campuses, and Airports: Port of Long Beach – Port of Long

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## Beach CAD/GIS Project Plan

- Innovation in Oil and Gas: Giprotymenneftegaz – Booster Pump Station With Preliminary Water Discharge Facility
- Innovation in Power Generation: Sargent & Lundy, LLC – Dry Fork Station Unit 1
- Innovation in Rail and Transit: Gautrain – Gautrain Rapid Rail Link
- Innovation in Road and Bridge: HDR Engineering, Inc. – Southwest Parkway Interchange
- Innovation in Utilities and Communications Networks: Technische Werke Friedrichshafen GmbH – sisNET Implementation at TWF-FN
- Innovation in Water Resources: Camp Dresser and McKee, Inc. – CDM Arbennie Pritchett Water Reclamation Facility
- Sustaining Society: Maharashtra Jeevan Pradhikaran – Transforming Intermittent Water Supply of Developing Countries to 24x7 System
- Sustaining the Environment: CH2M HILL – Round Butte Dam Selective Water Withdrawal
- Lifetime Achievement Award: John (Jack) Hallman, General Motors Corporation

## Academic Awards

- Educator of the Year: Peter Glaudemans – Eastern Technical High School
- High School, MicroStation PowerDraft: Caitlyn Alfred, Rob Davis, and RJ McNew – Eastern Technical High School – Turkey Hill Elementary School
- University/Collegiate, Engineering: Dominik Galica and Maciej Wlodarczyk – AGH University of Science and Technology – An Attempt at a Photorealistic Visualization of Mine Workings and Work of a Mechanical Coal Miner
- University/Collegiate, Architecture/Building: Tzu-Hung Ho – The School of the Art Institute of Chicago – Interspace
- Animation and Freeform/Artistic Expression: Maciej Wlodarczyk – AGH University of Science and Technology – Presentation of Interior of the Building With the Effect of the Light

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## ***Bentley's Communications Products Help Hellas Online Speed Delivery of 450-Kilometer Fiber Network in Athens***

29 May 2008

Bentley Systems, Incorporated announced that Attica Telecom, a telecom provider recently acquired by hellas online, a leading fixed telephony and broadband operator in Greece, has selected and deployed Bentley's communications software solution to design and build a 450-kilometer fiber network in Athens and other major cities in Greece, including Thessaloniki. The new network, which features a ring-based topology, will consist of more than 8,000 kilometers of fiber optic cable. The fiber optic rings are built out with spokes to key buildings and the conduit architecture is designed to handle a growing business through the addition of sub-conduits and incremental fiber optic cables after the initial build. Attica Telecom chose Bentley's communications solution, including Bentley Fiber and Bentley Inside

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Plant, because of its ability to interoperate, help plan infrastructure of this scale, ensure design and build phases are executed at the lowest possible cost, provide easy access to all project data via a repository-based system, and create streamlined workflows.

Attica Telecom had started the design process with a number of different desktop applications, including Microsoft Access, Visio, Excel, and static CAD drawings. It soon became clear that this was not a productive approach. As Mr. Dimitris Nikas, head of GIS and fiber optic documentation at Attica Telecom, explained, “Initially we worked with a series of products that were not interoperable and most of the design workflows required printouts and paper-based transactions to move things forward. This soon became unworkable because of the errors introduced into the process and because it was almost impossible to keep the data up to date across all of these programs and file types.”

Attica Telecom searched for a solution that would deliver cable information, access point information, duct information, splice enclosure information, building enclosure information, documentation of the fiber distribution panels, full documentation of the inside plant inventory, and customer information. In addition, it wanted to display this information graphically and make it readily available to all of the designers and construction engineers on the project.

The system also needed to be repository-based, allowing comprehensive data queries with easy data import and export tools. Moreover, it had to have the capability to support the correlation of logical events with a physical location.

“We chose the Bentley communications solution because the products are based on mature, tried-and-trusted technology,” said Mr. Nikas. “In addition, there was no need for any form of custom development – all we needed to do was configure the products to handle our data. As soon as we had installed the products and completed the initial setup, we were able to begin importing the existing data from our Access database and from Visio. At that point we were ready to go.”

All design work for the network is now done using Bentley Fiber and Bentley Inside Plant. Bentley Fiber, in particular, is used by many members of the hellas online team, from designers to field engineers and those dealing with customer issues.

[hellas](#) online has plans to deploy additional products in the Bentley communications solution, including Bentley Geo Web Publisher for Communications. This product presents maps and diagrams in a Web browser environment for nontechnical users like customer service representatives. It also will deploy the ProjectWise collaboration system to take advantage of its comprehensive workflow and documentation management capabilities.

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## ***CENIT Receives Contract From Airbus in the Double-Digit Millions***

26 May 2008

CENIT has recorded a key milestone in its future business performance by winning the largest individual contract that the Company has ever seen. The basic agreement, which was signed today, includes cross-border first level support for PLM systems. These services for user support relate to all PLM-oriented systems for over 20,000 employees at Airbus. The award of the contract to CENIT, which the Company won after long negotiations against fierce competition, underlines its technology, process and industry expertise. Of course this also stems from Airbus’ Power8 programme, which features worldwide IT harmonisation and related transnationalisation in addition to cost savings and quicker

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development cycles. For this reason, suitable IT suppliers were and are selected at EADS level on a global benchmark and are commissioned with specific projects by EADS' divisions. For EADS, focus on the core business and selection of long-term service partners play an important role.

“We have been working for EADS since 1999,” said CENIT member of the executive board Kurt Bengel, highlighting the great significance for CENIT. “The first projects totalled around €100,000. But this customer relationship has now taken on a new dimension. We have gained key market shares and substantially extended our position in the aerospace industry. Our investments in the customer EADS and in the qualification of our employees have been worthwhile in the long term. They were correct and necessary in order to successfully manage contracts of this scale. I am certain that this success will also be generated in other areas.”

“My praise goes to Kurt Bengel and his team, who have done an excellent job here,” added CFO Christian Pusch. We excellently and convincingly met the complex customer requirements. This only happens if you show above-average commitment in terms of the customer and the company.”

With this contract, CENIT will thus be providing the EADS Group with services involving first and second level support, training and education as well as methods and tool development. In addition to process development, the range of duties will now expand to include production and the entire process chain.

“Over the next year, we expect the number of CENIT employees used in ongoing projects to reach around 100. This is an exciting challenge for CENIT,” commented Kurt Bengel.

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### ***Ceon's PLM System Selected by Leading Converged Services Provider Siminn***

20 May 2008

Siminn, a leading provider of Fixed-line, DSL, Internet and Mobile services in Iceland, has selected the [Ceon](#) Product Control Center (PCC) product lifecycle management (PLM) software solution. Ceon PCC has been chosen to be the centralized product catalog to support order capture, product configuration, and fulfillment processes for all services delivered by Siminn.

Siminn will leverage PCC's standards-based capabilities to construct, manage, and deploy a layered product catalog that includes the full technical and commercial definition of products. PCC will be integrated with Siminn systems using the flexible integration facilities, and online product configuration and quote calculation support that PCC provides.

“Reducing time to market for new offers and simplifying order capture and fulfillment are key business objectives for Siminn,” said Sveinn Tryggvason, CIO of Siminn. “As part of our system transformation initiative we took a hard look at potential solution approaches before making our selection. Ceon's Product Control Center system will be a key building block in our SOA based architecture, and will allow us to unify our product model, and streamline order capture and fulfillment for all the services we offer.”

“We are pleased that Siminn has selected Ceon's solution and we are confident that Siminn will achieve its business goals with PCC being part of the systems environment,” said Peter Burke, CEO of Ceon. “Siminn's selection of PCC along with the successful PCC deployments we have completed this year is clear evidence that the adoption of PLM solutions is accelerating. We look forward to helping Siminn bring new offers to market more rapidly, and streamline ordering processes for these new offerings.”

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## ***HDR Increases Investment in Autodesk BIM Solutions***

27 May 2008

Autodesk, Inc. and [Avatech](#) Solutions, Inc. announced that HDR, Inc. is adding the Revit platform as one of its tools for building information modeling (BIM) to help increase productivity and foster greater interdisciplinary design collaboration across the firm. HDR has signed a three-year, multi-million dollar agreement that includes nearly 1,800 seats of Autodesk software plus consulting services from both Autodesk and Avatech Solutions. In addition to the Revit platform, the agreement also includes seats of AutoCAD Architecture and AutoCAD MEP software.

"HDR has been practicing BIM for more than three years," said Brandt Karstens, vice president, architecture director of systems for HDR. "The architectural practice is adding the Revit platform to fully embrace BIM's 3D modeling and database approach. Our Engineering practice is also using AutoCAD Architecture and AutoCAD MEP for our Water and Environmental and Resource Management Business Classes. The HDR management team sees this new agreement and Autodesk BIM solutions as keys to achieving our goal of becoming a completely BIM-based operation by 2010."

HDR is an employee-owned architectural, engineering and consulting firm with more than 6,800 professionals in over 150 locations worldwide. The firm is recognized for its expertise in a wide range of diverse project areas from healthcare and nanotechnology architecture to transportation, water/wastewater and environmental engineering. In addition to commercial projects, HDR's clients also include major federal government agencies such as the U.S. Army Corps of Engineers and the National Park Service. Utilizing the [Autodesk](#) platforms will help HDR continue to facilitate design collaboration throughout the firm and improve overall services by identifying constructability problems earlier in the design process, helping to reduce delays, and materials costs.

BIM is an integrated workflow built on coordinated, reliable information about a project from design through construction and into operations. By adopting BIM, architects, engineers, contractors and owners can create coordinated, digital design information and documentation; use that information to accurately visualize, simulate, and analyze performance, appearance and cost; and reliably deliver the project faster, more economically and with reduced environmental impact.

For a decade, Avatech Solutions, an Autodesk Premier Solutions Provider, has been helping HDR achieve their business objectives through innovative technology solutions. Avatech introduced the BIM concept to HDR, and now Avatech is partnering with Autodesk to accelerate HDR's transition to the Autodesk platforms. The two companies will collaborate on implementation planning, resource allocation and monitoring of progress toward attaining the goals envisioned by HDR.

"Avatech is a long-time advisor to industry leaders like HDR because our team is always thinking about how to leverage best-of-breed technologies to create significant improvements in business processes," says George Davis, president and CEO of Avatech Solutions. "Our deep-rooted industry experience, balanced with unrivaled technology expertise, will continue to enable us to propel businesses who want to raise the bar within their industry. We look forward to working with HDR for at least another ten years."

"HDR shares Autodesk's vision for BIM as the ideal process for integrated project delivery," said Jay Bhatt, senior vice president, Autodesk AEC Solutions. "We're particularly pleased to see leading firms such as HDR use our Revit platform, along with other Autodesk BIM solutions such as AutoCAD Civil

3D, to create a inter-discipline BIM practice that spans civil engineering, architecture, MEP engineering and construction."

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## ***Open-Silicon Adopts Magma's Hydra Automated Floorplanning and Hierarchical Design Planning Product to Streamline ASIC Implementation***

27 May 2008

Magma® Design Automation Inc. announced that Open-Silicon Inc., a leading fabless ASIC company, has adopted Magma's new Hydra™ floorplanning and hierarchical design planning product. Open-Silicon adopted Hydra because it enables development of production-quality floorplans that accurately reflect final chip architectural intent and performance. With this streamlined approach to ASIC planning, Open-Silicon can improve the quality of results and turnaround time of its customers' designs.

"Open-Silicon's mission is to provide our customers with predictable, reliable and cost-effective ASICs," said Dr. Satya Gupta, vice president of Engineering and Technology of Open-Silicon. "Hydra's advanced floorplanning capabilities, including automated partitioning, macro placement and macro shaping, can help us reduce turnaround time and improve quality of results. Hydra enables us to better capture architectural intent of the customer design and thus achieve a more predictable design handoff and implementation."

"Open-Silicon has enabled silicon success for leading semiconductor companies in a wide range of industry segments, including wireless and mobile, consumer, digital entertainment, computing, storage and networking, security and telecommunications," said Kam Kittrell, general manager of Magma's Design Implementation Business Unit. "We're pleased that the company and its customers are leveraging Hydra to reduce turnaround time and costs."

### **Hydra: Streamlining ASIC Handoff and Improving Designer Productivity**

Hydra is an automated floorplanning and hierarchical design planning product with complete physical optimization capabilities. Unlike other floorplanning tools, Hydra takes timing, power, congestion and area into consideration and generates production-quality floorplans, streamlining ASIC implementation. By automating top-down chip planning, floorplan generation, partitioning and physical block shaping, and then performing top-level clock tree generation and time budgeting for block-level implementation, Hydra dramatically improves designer productivity.

Hydra can be used in third-party flows or integrated with Magma's Talus® IC implementation system. Used in conjunction with Talus Design or Talus Vortex, Hydra offers integration from prototyping to implementation within the same data model. Hydra also works with RioMagic™, Magma's package-aware chip planning solution, to enable I/O planning and placement tradeoffs for both peripheral and flip chip packages.

Magma to Feature Hydra at DAC

To register for an in-depth Hydra demo at the Design Automation Conference, June 8-13 in Anaheim, visit Magma's website at <http://www.magma-da.com/DAC>.

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## ***Project-centric Doppelmayr/Garaventa Group moves faster with IFS Applications***

26 May 2008

Austrian-Swiss Doppelmayr/Garaventa Group, a leading supplier of ropeways and other rope-based passenger transport systems as well as systems for bulk material transport, has signed a contract to implement IFS Applications. The group typically runs about 150 – 200 tightly scheduled projects and aims to optimize project control and execution with IFS Applications. The initial contract is worth approximately 5 million Euros.

The new enterprise applications will replace a number of customized stand-alone systems that do not fulfill today's needs for an integrated, agile system which supports Doppelmayr/Garaventa's international operations. The Group has production facilities and sales and service locations in over 30 countries and to date has built almost 14,000 installations in 79 countries. As a project-centric engineer-to-order manufacturing company, forecasting, planning and timely delivery of work and material are essential and require an ERP system in which all activities can be monitored and tightly controlled.

“We are a global company, with projects growing in numbers, complexity and size. At the same time the project runtimes are getting shorter,” said Johannes Wittwer, Project Manager at Doppelmayr/Garaventa. “We chose IFS because of its strong functionality and understanding of our type of business as well as its track record in project-centric industries.”

Doppelmayr/Garaventa will implement IFS Applications components for engineering, manufacturing, project management, financials, human resources, distribution, business intelligence, document management and quality management. The project, which starts immediately, will be implemented step by step.

Industrial manufacturing is one of IFS' targeted market segments. IFS has a broad customer base in the industry, focusing on large to medium-sized manufacturers that need support for complex order-driven and mixed-mode manufacturing across the extended enterprise. With strong support for project management, engineering, constraint-based scheduling, product configuration, and after sales, IFS' component-based business solutions are designed to help companies improve quality, contain costs, and improve their competitive position in manufacturing industry segments such as machinery, defense, fabricated metal products, and plastics.

### **About Doppelmayr/Garaventa**

The Doppelmayr/Garaventa Group is the world quality and technology leader in ropeway engineering. The Group develops efficient passenger transport systems for summer and winter tourism resorts as well as passenger transport systems for the urban environment, airports, shopping malls, sports complexes, adventure parks, trade fairs and other facilities. Furthermore, the Group also offers material transport systems for bulk material and unit load transport. The Group has production facilities, and sales and service locations in over 30 countries of the world and to date has built almost 14,000 installations in 79 countries. Turnover amounted to € 659 million and the company had more than 2,400 employees in fiscal year 2006/2007.

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## ***STARC Adds Sequence Low-Power Tools to Advanced Design Flow***

27 May 2008

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Sequence Design announced that its PowerTheater and CoolTime low-power tools have been integrated into the latest release of the Japanese Semiconductor Technology Academic Research Center (STARC) advanced design flow, STARCAD-CEL Version 2.0.

STARCAD-CEL Version 2.0, addressing the challenges of very advanced process technologies including 65nm and 45nm, has been updated to emphasize low-power design, particularly RTL power analysis and reduction, and power integrity at physical implementation. The STARCAD-CEL Version 2.0 design methodology is shared by the leading Japanese semiconductor companies that comprise STARC's membership as a standard digital design platform.

“It is vital to control power in the earliest stages of the design,” said Nobuyuki Nishiguchi, Vice President and General Manager, Development Department-1 at [STARC](#). “This requires the accurate RTL power analysis and power reduction capabilities provided by a proven tool such as Sequence's PowerTheater.”

Sequence's CoolTime has also been validated for STARCAD-CEL Version 2.0. Nishiguchi praised CoolTime's ability to significantly reduce both dynamic voltage drop and leakage power in advanced designs.

“It is indeed an honor to have been associated with the STARC team for over four years in creating reference flows for the Japanese design community,” said Vic Kulkarni, President & CEO of Sequence Design. “Their technical guidance has been very valuable to our R&D team in shaping the DFP product line. We are looking forward to many years of collaboration ahead as we embark on solving the sub-45nm power management challenges in SoC designs.”

## **About PowerTheater/PowerTheater-Explorer**

PowerTheater is the industry's first RTL power analysis and power prototyping solution with the singular ability to accurately analyze power at RTL and support power management techniques such as voltage islands, mixed voltage threshold, power gating, and clock gating. PowerTheater recently added support for the Si2 CPF standard along with the following new features:

- Control all aspects of running PowerTheater through a single Tcl-based command file.
- Identify high-power windows utilizing comprehensive simulations from hardware accelerators.
- Compute full-chip, gate-level power efficiently using RTL simulations.
- Prevent voltage-drop related test and functional failures by automatically identifying critical vectors from multiple simulations.

PowerTheater-Explorer is a capability that adds state-of-the art power visualization and debug features for fast, interactive RTL power analysis. A new SmartSource Viewer allows designers to determine hot spots in the design, to visualize, debug and interactively analyze a design's power consumption. The hierarchical RTL power tree display shows hot spots that can be cross-probed to schematics, showing connectivity and indicating how activity is moving through the design and how instances impact one another. These results can be displayed and analyzed at RTL, gate, or mixed levels of abstraction. SmartSource also provides a dedicated view of the clock tree for fast analysis and tracing of clock nets in the design. For more information, visit: <http://www.sequencedesign.com/solutions/powertheater.php>.

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## ***Technische Werke Friedrichshafen Selects Bentley Software to Implement New Multi-Utility GIS***

30 May 2008

Bentley Systems, Incorporated announced that Technische Werke Friedrichshafen (TWF), a regional multi-utility in southern Germany supplying electricity, gas, water, and heat to 36,000 energy users, has implemented a geographic information system (GIS) based on [Bentley](#) sisNET. The new system, which also uses Bentley sisFLOW, [Bentley PowerMap Field](#), Bentley sisVIEW, Bentley sisIMS, and [Bentley Geo Web Publisher](#), replaces an Intergraph FRAMME system.

With its new Bentley-based GIS, TWF is able to support its engineering and regulatory compliance operations more efficiently. In particular, the new system is providing enhanced support of its designers and field engineers, thematic and statistical analyses for the German utilities regulator, and integration with SAP. Bentley sisNET provides TWF with both textual and graphical reporting.

“Given the fact that our GIS is connected to numerous technical, commercial, and regulatory compliance workflows, the decision to switch from a system like our company’s former GIS to Bentley sisNET required careful consideration,” said Herr Walter Mogg, head of the surveying and GIS department. “However, the proven editing functions of [MicroStation](#) at the core of Bentley sisNET, along with its multi-utility data models and comprehensive analytical capabilities, made it the ideal choice. Bentley sisNET generates consistent and high quality data that facilitate the analyses and reports that are so critical in the utility market.”

Herr Mogg continued, “The utilities industry faces new regulatory compliance challenges. Before deploying the Bentley GIS, we had to gather this information manually, which added significant costs to our operations. Bentley sisNET offers broad query and export functionality, enabling us to meet the regulatory requirements and to enjoy substantial savings.”

A web-based information system built on Bentley sisIMS is helping TWF avoid costly, fault-prone data conversion. All users now work with a single data source that is known to be accurate.

Bentley sisNET combines cadastral information with GIS-based infrastructure engineering functionality for electricity, gas, water, and district heating networks. The consistent, high-quality data generated supports numerous business processes throughout the TWF organization. As a result of the documentation and operations support services it offers for smaller utilities on an outsourced basis in the surrounding region, coupled with improved operational efficiencies in its own business, the company expects its investment in its new GIS to pay for itself in less than two years.

### **About Technische Werke Friedrichshafen**

Technische Werke Friedrichshafen (TWF) is a regional multi-utility supplying electricity, gas, water, and heat to 36,000 energy users in Friedrichshafen on Lake Constance in southern Germany. The company’s 205 employees generate approximately \$150 million in revenue annually and ensure that its products and services are safe, reliable, cost-effective, and available when its customers need them.

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## ***Tompkins Industries Selects CDS Solution***

27 May 2008

Catalog Data Solutions ([CDS](#)) announced that Tompkins Industries, Inc. has selected its CAD model download and Catalog solution.

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Tompkins Industries, headquartered in Kansas, operates 10 distribution centers across the US and specializes in hydraulic adapters and fluid power products — offering the broadest range of adapters in the industry. Responding to customer requests for CAD models of its products, Tompkins Industries decided to offer 3D CAD models for download from its website. “We frequently get requests from customers for CAD models,” said Bill Hartman, National Sales Manager, Tompkins Industries. “By adding 2D/3D CAD drawings/models to our website, we are able to save design engineers valuable time in meeting their product design deadlines and as a result we expect to see an increase in the number of online sales leads and RFQs.”

“We are delighted to have been selected by Tompkins Industries for our leading online CAD model download and digital Catalog solution”, said John Major, CEO Catalog Data Solutions, “online 3D models are an important sales and marketing tool for all industrial suppliers and distributors. With many customers moving from 2D to 3D CAD systems providing online 3D CAD model downloads often ‘locks’ products into a design so suppliers later benefit from the sales success of that design. Conversely suppliers without 3D models on their website are at risk of losing customers to their competition who do offer 3D models”.

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## ***Volkswagen Group Relies on ESI Group’s Virtual Performance Solution***

27 May 2008

The [Volkswagen Group](#) has taken on the Virtual Performance Solution software suite developed by ESI Group for the simulation of vehicle crash, safety and structural load cases. Virtual Performance Solution includes PAM-CRASH, PAM-SAFE products and FPM (Finite Point Method) technology for all vehicle crash and safety simulations, as well as the latest developments in implicit technology for linear and nonlinear structural load cases. The most recent software release – Virtual Performance Solution version 2008 – opens the way to a considerable acceleration in the development process. Impact scenarios and structural load cases as designed, as analysed, as built and as destroyed can now be simulated much more efficiently. This results from ESI Group’s close cooperation with the Volkswagen Group, Europe’s largest automobile manufacturer: both have jointly pioneered crash simulations successfully since 1986.

Dr. Ralph Sundermeier, Head of Functional Calculation and Methods at Volkswagen, is counting on considerable improvements in the development process thanks to VP: “With this further development of the software suite, ESI Group has achieved a quantum leap in the field of simulations. By being able to utilise a unique mathematical model for both, crash and load testing, we’re improving our development efficiency at several stages of a project. Given the ever-shortening development cycles in the automobile sector, this gives us a decisive competitive edge.”

Vincent Chaillou, President and COO of [ESI Group](#), expresses his great pleasure with the car manufacturer’s decision: “Volkswagen has once again shown its trailblazing role in the simulation field by actively supporting the further development of ESI’s software suite. It is our very close cooperation that has allowed us to make these great advances. Volkswagen’s decision to introduce Virtual Performance Solution in their product development process is therefore also an expression of the strategic partnership that has been forged between our companies in the last two decades.”

ESI Group’s software suite enables the realistic simulation of product behavior in varying environments. With Virtual Performance Solution version 2008, based on explicit and implicit technologies, the process of safety checking and behavior of structures under operating conditions can be

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comprehensively simulated in all phases of vehicle development. ESI Group's Virtual Performance Solution will help the Volkswagen Group enhance their development capacities and reach earlier the stage of physical prototype test procedures.

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## ***Zensys Selects Berkeley Design Automation Analog FastSPICE™ for Wireless Transceiver Verification***

28 May 2008

Berkeley Design Automation Inc., provider of Precision Circuit Analysis™ technology for advanced analog and RF integrated circuits (ICs), announced that Zensys, developer of the [Z-Wave](#) wireless home control standard and a family of low-cost, low-power integrated transceiver chips embedded with Z-Wave, has selected the company's Analog FastSPICE™ circuit simulator for complex-block characterization and full-circuit performance simulation of its fully integrated RF communication module.

“Verifying our RF communication module requires extensive transistor-level simulation of a complete RF transceiver with the highest possible accuracy,” said Tony Shakib, Chief Executive Officer at Zensys. “Analog FastSPICE completed characterization of our RF transceiver, including a complex sigma-delta modulator, more than 5x faster than our traditional SPICE simulator with true SPICE accuracy. With Analog FastSPICE we will be able to tapeout with higher confidence and get our products to market faster.”

[Berkeley Design Automation](#) tools include Analog FastSPICE™ circuit simulation, Noise Analysis Option™ device noise analyzer, RF FastSPICE™ periodic analyzer, and PLL Noise Analyzer™. The company guarantees identical waveforms to the leading "golden" SPICE simulators down to noise floor (typically 0.1% or less) while delivering 5x-10x higher performance and 5x-10x higher capacity. It achieves this by using advanced algorithms and numerical analysis techniques to rapidly solve the full-circuit matrix and the original device equations without any shortcuts that could compromise accuracy.

Design teams from top-10 semiconductor companies to leading startups use Berkeley Design Automation tools to solve big analog/RF verification problems. Typical applications include characterizing complex blocks (e.g., PLLs, ADCs, DC:DC converters, PHYs, Tx/Rx chains) and running performance simulation of full circuits (e.g., wireless transceivers, wireline transceivers, high-speed I/O macros, memories, microcontrollers, data converters, and power converters).

“We are excited that Zensys selected Analog FastSPICE for the verification of their RF technology for interoperable home control devices,” said Ravi Subramanian, president and CEO of Berkeley Design Automation. “Effective design verification of complex analog/RF circuits for ultra-low power RF mesh networking products requires true SPICE accuracy to enable delivery of high-quality products to the consumer market. We are proud to help Zensys dramatically accelerate their analog/RF circuit verification.”

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## ***Zuken Boosts Presence in Defense Market Announcing Deal with MBDA***

27 May 2008

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Over recent years, Zuken's customer base within the electronics market has expanded beyond the traditional stronghold of consumer and automotive sectors to establish significant presence within the defense industry. The recent move to Zuken's solutions for schematic capture and PCB design by European Missile Systems conglomerate, MBDA, is another feather in Zuken's cap with one more big name backing Zuken's defense capabilities.

The design centers within MBDA originated from many different predecessor organizations across Europe – within the EADS, BAE Systems and Finmeccanica shareholder community. With competitive pressures in the industry growing, ECAE tool harmonization on a multinational scale was the obvious solution for increased performance and output. The objective was to select one solution for schematic and PCB design that would span all design teams. Having experienced success in France with Zuken's cabling solution E<sup>3</sup>.cable, MBDA had the confidence in Zuken to deliver a single integrated design platform across Europe.

Andy Inkersole, Head of Electronic Design ECAE from MBDA, commented: "Zuken proved with CR-5000 and E<sup>3</sup>.cable that they could cost effectively deliver the full harmonization within our current environment, while offering flexibility and technical competence."

The initial five-year agreement with MBDA sees the company moving all PCB design, schematic capture and electrical design to Zuken's enterprise-wide modular PCB design solution, CR-5000 and E<sup>3</sup>.cable. The first phase of this will begin with implementation in France and the UK, and will later lead to full rollout in Italy, and finally Germany.

Zuken's latest agreement with the major European defense contractor MBDA adds to their rapidly growing customer base in this sector.

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## Product News

### ***Altair and FTSS Cooperate to Deliver Integrated Safety Analysis Solution***

27 May 2008

Altair Engineering, Inc. announced a long term agreement with First Technology Safety Systems, Inc. (FTSS), the world's leading designer and manufacturer of sophisticated crash test dummies and their virtual counterparts, to integrate and deliver FTSS crash dummy models with Altair's RADIOSS crash analysis software. The agreement provides the foundation to jointly develop and deliver a highly integrated, accurate safety analysis solution, specifically tailored to meet the needs of the global crash analysis community.

Under the agreement, [Altair](#) will have responsibility to market, sell, and provide first-line support for FTSS dummy models that are provided with RADIOSS. The agreement also outlines close collaboration between Altair and FTSS software development teams to harmonize RADIOSS with the FTSS virtual dummies to deliver a high quality, well-correlated safety analysis solution. FTSS dummy models will be directly supported in HyperWorks modeling and visualization products including HyperMesh and HyperView as well as HyperCrash, a highly tuned modeling environment for crash analysis and safety evaluation.

"Altair is firmly committed to delivering world class safety performance solutions," says James R. Scapa, president and CEO of Altair Engineering. "As such, we recognize the importance of high fidelity

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dummy models deeply integrated with our core product set. This need is clearly best served through partnering, at both a business and technology development level, with the premier dummy manufacturer in the world."

"FTSS is pleased to offer its high fidelity dummy models in the RADIOSS solution for our joint customers," says Christopher J. O'Connor, president and CEO of FTSS. "We are proud to partner with a global leader in the field of Computer Aided Engineering and Analysis, and confident that together we will deliver optimized CAE solutions in the field of occupant safety and crashworthiness."

"High quality finite element dummy models are critical to the virtual safety design process," says Djamal E Midoun, NAE unibody safety manager at Ford Motor Company. "I am glad to see a partnership between [FTSS](#) and Altair in developing such models, it will provide us with an important enabler to meet the increasingly demanding external as well as Ford internal requirements, with less time and fewer physical prototypes."

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## ***Altair Increases Western European Presence through FEMTO Channel Partner Agreement***

26 May 2008

[Altair Engineering](#), Inc. announced a new channel partner agreement with FEMTO Engineering, a Dutch software and engineering service provider. The agreement will allow FEMTO to sell the complete HyperWorks platform and related services to customers in the Netherlands, Belgium and Luxembourg. The partnership extends Altair's reach while offering customers in the aerospace, automotive, manufacturing and equipment design industries local support and professional consulting services.

"FEMTO is pleased to add Altair's industry-leading HyperWorks platform for computer-aided engineering to our offerings," said Ronald van Dijk, director of FEMTO Engineering. "We look forward to the partnership with Altair and see the HyperWorks platform as a natural fit with our current engineering software and services. We are especially excited to increase our capabilities to help large, global companies perform all types of enterprise simulations, as well as medium and small-sized companies."

"The Netherlands have proven to be an important market, with a diverse range of companies and a high growth potential," said Dr. Detlef Schneider, managing director of Altair Engineering, Germany. "We are currently serving this market from our office in Cologne, Germany. We were looking for an experienced partner to expand our sales bandwidth in the Benelux area, and we are happy to see [FEMTO](#) Engineering join our partnership program. We will benefit from the customer base FEMTO has in the Benelux region, their experienced CAE team, and their solid infrastructure for sales, support and training. Our local customers in the Netherlands and Belgium will also appreciate service and support in Dutch, rather than in English or German."

"FEMTO Engineering has a broad customer base in the offshore and ship building industry," said Tom Santegoeds, director of FEMTO Engineering. "We will be better able to serve those customers by adding HyperWorks to our software portfolio, and we also see a large potential for these tools in the aerospace and automotive markets in our region."

Built upon a foundation of design optimization, performance data management and process automation, HyperWorks is an enterprise simulation solution for rapid design exploration and decision-making. As the most comprehensive, open-architecture enterprise analytics solution in the industry, HyperWorks

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includes best-in-class modeling, analysis, visualization and data management solutions for linear, nonlinear, structural optimization, fluid-structure interaction, and multi-body dynamics applications.

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## ***Always in Touch: OptiTex Packs Even More Features into its 3D Runway Designer 10***

22 May 2008

OptiTex has made its best-of-breed software even better by enhancing one of its core products, 3D Runway Designer 10, with remote collaboration tools.

“We’ve integrated several new features into Runway 10 to smooth the collaboration between designers and production professionals,” states Ran Machtinger, President and CEO of OptiTex, Ltd. “Chief among the improvements is an open chat room, which allows production and image files in 2D and 3D to be sent between partners participating in the design process. They can then easily modify these images using our new chalk tool, allowing design and production staff to draft on-screen together, in real time from remote locations.”

These new and upgraded features represent another step forward in the OptiTex revolution, allowing virtually all of the design process to be performed digitally.

3D Runway Designer 10 has new customizable features and options, allowing designers the freedom to change textures, materials, colors and stitches or add logos on screen without using a single piece of fabric. The garment can then be seen “in action” on OptiTex's 3D models, all of which are fully adjustable to reflect any body type.

OptiTex’s on-screen environment is customizable to enhance the designer’s creative process. The accuracy of the images eliminates material, energy and time waste, providing rapid ROI as well as an environmentally friendly application.

The new, collaborative features in 3D Runway Designer 10 dovetail perfectly with another area in which OptiTex has experienced a surge in growth: Utilization as an essential learning tool in educational settings, ranging from renowned universities to vocational training centers.

“Over the last two years we’ve seen phenomenal acceptance of our software worldwide by major educational institutions. They need to provide their students with a strong background in the latest design tools, allowing them to be immediately employable upon graduation. Now, instructors can work remotely with students in an e-learning environment to ensure their projects are viable as they prepare to enter the workforce,” concludes Machtinger.

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## ***Bentley Announces ISO 15926-Based Bentley OpenPlant PDx Manager***

29 May 2008

Bentley Systems, Incorporated announced Bentley OpenPlant PDx Manager, a new software tool built on ISO 15926 that makes existing plant design system data, such as PDS data, open and interoperable with other applications. With Bentley OpenPlant PDx Manager, engineering/procurement/construction firms, owner-operators, and suppliers can connect to a live PDS project and open up the entire PDS project database without affecting the underlying PDS model or data. In addition, they can access in real

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time all model data and deliverables and extract them to ProjectWise in order to support globally distributed workflows. Because Bentley OpenPlant PDx Manager makes Bentley applications interoperable with PDS databases, the ability to access and share this data in a collaborative environment doesn't require PDS; the data is available to anyone using the next-generation of MicroStation and ProjectWise. Users also can save data in the ISO 15926 format, allowing them to connect to a PDS project and reuse its content in ISO 15926-compliant applications such as ProjectWise Lifecycle Server or Bentley OpenPlant PowerPID. With the project data in an open format and managed by ProjectWise, project teams can easily collaborate downstream and upstream, even when disparate systems are deployed. Benefits of Bentley OpenPlant PDx Manager to the organization include managed data across the asset lifecycle, streamlined workflows, lower project and asset lifecycle costs, compressed schedules, and increased overall productivity – without adding any risks to the project.

Rob Whitesell, vice president, Bentley Software, said, “Bentley’s commitment to open standards and interoperability among applications is long standing and ongoing. This past January we introduced Bentley OpenPlant, the first range of software products for the creation and management of plant infrastructure based on the ISO 15926 data model. These products facilitate distributed-enterprise collaboration spanning disparate systems and include Bentley OpenPlant PowerPID, the only commercial P&ID solution to be based on a completely open data model. Today, we are announcing Bentley OpenPlant PDx Manager, which brings interoperability to users of data created with PDS, streamlining their workflows and improving their productivity. In due course, we will introduce similar support for other plant design systems.”

With Bentley OpenPlant PDx Manager, users have real-time access to all PDS data via a live link to the PDS project database without translations, conversions, or extractions. They can see the breakdown structure of the model in a hierarchy browser, select individual objects, components, lines and tags, and zoom into the selected item. For each item in the model, a property pane displays all of the information in the same order in which users would see the data within PDS itself. This ability to see the live data associated with any component, rather than a snapshot review model, reduces errors and rework due to out-of-date information, saves project costs, and improves productivity.

Bentley OpenPlant PDx Manager also enables project data created with PDS to be saved in the ISO 15926 format, extending its useful life beyond that of the PDS application. The information can be accessed and managed throughout the lifecycle of the infrastructure asset – from design to engineering, construction, operations, and maintenance.

For more information about Bentley OpenPlant PDx Manager, visit <http://www.bentley.com/openplant>.

For more information about Bentley OpenPlant PowerPID, visit <http://www.bentley.com/PowerPID> For more information about Bentley OpenPlant and to download a free whitepaper on ISO15926, visit <http://www.bentley.com/openplant>.

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## ***Bentley Integrates Bentley Building Electrical Systems With Visual Lighting Design and Analysis Software From Acuity Brands Lighting***

29 May 2008

Bentley Systems, Incorporated announced the integration of Bentley Building Electrical Systems, a professional engineering design application in Bentley’s suite of Building Information Modeling (BIM) products, with Visual, the leading lighting design and analysis software from [Acuity Brands Lighting](#).

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Through this integration, those using Visual for lighting design and analysis will have immediate access to all building geometry and design data in the BIM model. The completed detailed luminaire placement and lighting design data can then be brought into the model to produce accurate 3D representations and detailed design and construction documentation. At the same time, electrical load data can be used for circuiting design and power and energy analysis, and IES photometric files can be brought back into the BIM model to facilitate lighting visualization.

Said Rob Whitesell, vice president, Bentley Software, “This joint development will enable lighting designers to easily share data and work collaboratively with other building electrical system disciplines. The improved efficiencies and ready access to accurate data will increase productivity and eliminate many of the errors that plague workflows associated with uninteroperable software. Thus, project quality will be enhanced and costs reduced.”

The Visual Professional Edition is a comprehensive lighting analysis tool designed for demanding interior and exterior applications. It combines an advanced 3D interface with the latest in radiosity theory to provide efficient and highly accurate analyses of complex architectural spaces. The new lighting calculations in Visual 2.6 are 10 to 100 times faster than previous releases of Visual, facilitating the completion of complex lighting projects.

Bentley Building Electrical Systems is based on MicroStation and complements Bentley Architecture, Bentley Building Mechanical Systems, and Bentley Structural to provide a comprehensive suite of BIM solutions for the building industry. Its key features include automated symbol placement, circuiting and labeling; raceway design; cable/circuit routing and management; automated generation of riser diagrams and drawing legends; and reporting including schedules, bill of materials, quantity take-offs, and cost estimation. The product supports 2D/3D workflows, “step-wise refinement” of the design process, and BIM methodology.

To learn more about Bentley Building Electrical Systems, visit <http://www.bentley.com/electrical>.



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## ***Bentley Releases Bentley Water V8 XM Edition***

29 May 2008

[Bentley](#) Systems, Incorporated announced the release of Bentley Water V8 XM Edition. This comprehensive water distribution design software helps engineers and GIS professionals in water utilities and municipalities design, document, and manage potable water distribution networks more efficiently. Bentley Water V8 XM provides all the capabilities of Bentley Map including map management, thematic and overlay analysis features, business and topological rules enforcement, and accurate editing. In addition, it allows easy access to enterprise data to improve the management of the asset lifecycle. Because Bentley Water V8 XM integrates with Bentley’s Haestad Methods hydraulic modeling and analysis software, users of WaterGEMS, WaterCAD, and HAMMER can share network connectivity, maintenance records, and operational data to run hydraulic simulations of their potable water distribution systems. Bentley Water V8 XM also interoperates with Bentley Geospatial Server for enterprise GIS implementation and enterprise collaboration, Bentley PowerMap Field to support field technicians using offline data, and Bentley Geo Web Publisher for publishing water infrastructure data to internal and external websites.

“Bentley continues to lead and innovate with this latest addition to its comprehensive solution for potable water networks,” said Olcay Ebcin, GIS manager, Istanbul Water & Sewerage Administration,

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which serves a city with a population of more than 11 million. “As users of both Bentley Water and WaterGEMS, we welcome Bentley’s efforts to provide us with a tightly integrated workflow between hydraulic modeling, asset management, and GIS.”

Commenting on the new software, Jack Cook, Bentley vice president, said, “The release of Bentley Water V8 XM broadens Bentley’s support of potable water infrastructure – from sources to treatment plants, transmission, and distribution networks, right through to the consumer. It includes the powerful capabilities of other Bentley mapping, data management, collaboration, plant design, hydraulic simulation, construction documentation, and field engineering tools to deliver the industry’s most comprehensive water solution for investor-owned utilities, civil engineering consultants, and municipal utilities.”

Bentley Water V8 XM Edition uses geospatial XML Feature Modeling (XFM) fully. This paradigm shift in the development of GIS applications empowers users to completely customize functionality and the predefined schema, as well as to choose from a wide range of persistent data models including stand-alone DGN, DGN/RDBMS, and Oracle® Spatial. When used concurrently with Bentley Geospatial Server, Bentley Water supports simultaneous multiuser editing, optimistic and pessimistic transactions, and time component features, including versioning and live or disconnected mode editing, provided by Oracle®.

The XFM environment enables a new set of asset management, water network maintenance, and operation capabilities. These include smart placement and network connectivity, configurable isolation tracing and reporting, dynamic property-based symbology and annotation, and network leak analysis tools.

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### ***DesignAdvance Introduces CircuitProbe - New Product Enables Bi-Directional Communication Between Layout and Schematic***

27 May 2008

DesignAdvance® Systems, Inc. announced the release of CircuitProbe. The new product enables bi-directional communication, through IPC, between layout and a PDF of the schematic. The tool gives both Designers and Engineers a mechanism to select one or more design elements in either Cadence Allegro Viewer environment or a PDF of the schematic and have the corresponding design elements selected in the other application.

“As a current [DesignAdvance](#) customer, we are impressed with the productivity gains our designers achieve with CircuitSpace,” stated Greg Bodi, Senior Manager at NVIDIA Corporation. “Now with CircuitProbe, we see the combined solution being utilized by both our designers and engineers and adding value throughout our entire PCB design cycle,” continued Mr. Bodi.

Leading technology companies recognize the value CircuitSpace’s placement automation technology reductions in PCB design time have a direct impact on time-to-market for new products, which directly correlates to profitability. “CircuitProbe is a natural extension of our patented CircuitSpace technologies,” said Edward Pupa, DesignAdvance CEO.

CircuitProbe Featured Capabilities:

- Bi-directional Cross-Probe between Layout and (PDF) Schematic

- Interactive clustering
- Save physical design templates
- Population of any Allegro selection set
- Verify location of design elements in either application

## Availability

CircuitProbe is now available.

For sales within North America, please visit EMA Design Automation at <http://www.ema-eda.com>

For sales outside of North America, please visit our distribution partners at <http://www.designadvance.com>

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## ***ESI Group Adopts Oracle to Power ESI Group Decision Support System Enterprise Solutions***

27 May 2008

[ESI Group](#) announced that it will be working with Oracle to meet the growing demand for enterprise based computer-aided engineering (CAE) and analysis in the PLM environment. ESI Group will empower its [VisualDSS](#) solution with the latest technologies and product architectures from Oracle in order to optimize its answer for simulation process and data management.

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## ***EVE Unleashes DW-FPGA for FPGA Synthesis***

27 May 2008

[EVE](#) announced availability of DW-FPGA, a DesignWare® foundation library for use with field programmable gate array (FPGA) synthesis software.

DW-FPGA offers register transfer level (RTL) source code for the most commonly used DesignWare Foundation Library intellectual property (IP) components from Synopsys Inc. DW-FPGA supports XST® from Xilinx, Quartus® from Altera, Synplicity's SynplifyPro® and Precision™ from Mentor Graphics Corporation, along with languages Verilog, VHDL and SystemVerilog. It can be used with or without EVE's ZeBu and for simulation, emulation, prototyping and FPGA designs. It will be demonstrated, along with EVE's entire line of hardware/software co-verification solutions, during the 45th Design Automation Conference (DAC) in booth #301 June 9-12 at the Anaheim Convention Center in Anaheim, Calif.

When implementing an application specific integrated circuit (ASIC) design on an FPGA or an FPGA prototyping board, the original ASIC design often uses DesignWare Foundation components not readily available for an FPGA implementation. With DW-FPGA, whenever ASIC-based RTL code references a DesignWare component of the foundation library, the synthesis tool automatically uses the synthesizable definition contained in the provided source code to infer an optimized FPGA description. DW-FPGA includes typical DesignWare functions, including adders, subtractors, multipliers, dividers, cosines and squareroot.

"We welcome the initiative of EVE to bring to market DW-FPGA," notes Steve Lass, director of

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software product marketing at Xilinx. "The combination of Xilinx ISE® Design Suite with EVE's DW-FPGA library of synthesizable models will allow our customers to quickly implement their ASIC designs onto Xilinx FPGAs."

Adds Mike Dini, founder and president of The Dini Group: "Mapping DesignWare functions into FPGAs has long been a cumbersome, expensive, and mysterious undertaking. Worldwide demand for FPGA-based ASIC prototyping, ASIC emulation, and hardware/software co-simulation is increasing exponentially. The demand for cost-effective, third-party solutions for this chore is needed. By offering low-cost support of DesignWare models in FPGAs, EVE solves this irritating issue."

"EVE is focused on ZeBu, our fast emulation technology," says Dr. Luc Burgun, EVE's chief executive officer and president. "However, DW-FPGA is the first EVE offering serving the wider FPGA community with more to come. These products will not be limited to ZeBu users."

## **Pricing and Availability**

DW-FPGA is priced at \$5,000 for a one-year, time-based license provided as a site license.

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## ***GibbsCAM Post Processors for Mori Seiki NMV Line Approved***

27 May 2008

[Cimatron Limited](#) announced that the set of its GibbsCAM® product's post processors that was developed for Mori Seiki's NMV line of machine tools has been validated by Mori Seiki. This collaborative effort between the two companies ensures that GibbsCAM's posted output supports the 5-axis simultaneous capabilities of these machine tools. As competitive pressures grow, the market demand for 5-axis simultaneous machine tools is also growing as they significantly reduce setups and improve overall part quality.

"Accurate post processors are still the Achilles tendons of today's advanced machine tools," explains Bill Gibbs, founder and CEO of Gibbs and Associates. "This is certainly the case when dealing with 5-axis machine tools. Working directly with Mori Seiki DTL, we were able to significantly reduce the time it took to develop the set of post processors for their NMV series."

The Mori Seiki NMV series of machine tools are high-precision, 5-axis control vertical machining centers. Using Mori Seiki's DCG® (Driven at the Center of Gravity) technology, vibration is controlled allowing for high precision at high speeds. DCG virtually eliminates vibration ensuring a high quality surface finish, outstanding acceleration performance and improved roundness. DCG along with Mori Seiki's DDM™ (Direct Drive Motor), Table-in-Table Construction and ORC™ (Octagonal Ram Construction) provide the NMV series with the power and stability required for today's demanding machining applications.

In addition to developing post processors for the Mori Seiki NMV line, Gibbs has also developed accurate machine models of the NMV8000 DCG and NMV5000 DCG for use with GibbsCAM's Machine Simulation capability. Machine Simulation allows program motions to be fully simulated to identify potential programming errors which would result in expensive machine tool crashes on the shop floor.

For more information about GibbsCAM or to locate your local GibbsCAM Reseller, go to <http://www.GibbsCAM.com>, call 1-800-654-9399, or email [info@GibbsCAM.com](mailto:info@GibbsCAM.com). For more

information about Mori Seiki and their line of machine tools, visit [http:// www.MoriSeiki.com](http://www.MoriSeiki.com).

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## ***Mentor Graphics Qualifies Calibre Model-based Planarity Flow for TSMC's 65 and 40 nanometer IC Manufacturing Processes***

29 May 2008

Mentor Graphics Corporation announced that its SmartFill model-based planarity flow has been qualified for TSMC's 65 and 40 nanometer (nm) processes. By reducing metal thickness variability, model based metal filling can help improve systematic and parametric yield. The SmartFill flow integrates the Calibre® CMPAnalyzer and YieldEnhancer facilities, and TSMC's DFM (design for manufacturing) data kit (DDK). The SmartFill qualification is a continuation of the DFM between [Mentor Graphics](#) and TSMC under TSMC's Active Accuracy Assurance (AAA) program.

"Making the best use of foundry data in an accurate and consistent manner with EDA partners is a goal of our AAA initiative. We envision further collaboration with Mentor Graphics as AAA expands to capture more of advanced process technologies' inherent value," said S.T. Juang, Sr. Director of Design Infrastructure Marketing, TSMC.

The Calibre CMPAnalyzer tool is the foundation of the Mentor CMP solution. CMPAnalyzer is directly linked with VCMP, TSMC's CMP simulator, to provide 3-D hotspot detection and to determine an optimum filling strategy. CMPAnalyzer then passes the appropriate information to the SmartFill function of the Calibre YieldEnhancer tool, which adds the fill elements directly into the layout design database. The combination of accurate foundry data and intelligent fill analysis improves parametric yield by reducing thickness variation that affects resistance while minimizing the capacitance added to the design. CMPAnalyzer also generates thickness values that can be incorporated into the Calibre xRCTM tool for more accurate parasitic extraction. CMPAnalyzer and YieldEnhancer are built on the Calibre nm Platform.

"As we move beyond 65nm, tight linkages between EDA tools and manufacturing processes becomes more and more critical," said Joseph Sawicki, vice president and general manager, Design to Silicon Division, Mentor Graphics. "With CAA, litho process checking, and CMP tools qualified at TSMC, the Calibre physical verification platform provides a comprehensive approach to yield management at advanced process nodes."

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## ***Spatial Announces Integrated Suite to Speed EDA 3D Analysis Development***

20 May 2008

Spatial Corp. announced the availability of the EDA 3D Analysis Suite which enables rapid development of 3D analysis and visualization features within current EDA physical design flows. The software component suite provides productivity and time-to-market (TTM) advances to tool developers with an integrated set of modeling, visualization and meshing utilities accessible through a unified API, streamlining development while eliminating much of the need to understand 3D geometry modeling.

"Spatial has been providing 3D geometry components and services to EDA industry leaders like Agilent, Ansoft, CST, Synopsys and Zuken for many years. More recently, wireless devices, advanced packaging

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and manufacturing variability are accelerating the need for automated 3D analysis and visualization. The EDA 3D Analysis Suite provides a ready-made solution that enables tool suppliers to rapidly address their customer's requirements and bring industry leading solutions to the market more rapidly," stated Keith Mountain, Spatial's CEO. "Spatial provides the 3D expertise, allowing EDA vendors and their developers to remain focused on their core competencies."

EDA applications benefiting from 3D analysis include high frequency board and package signal integrity, embedded memory and passive component characterization, lithographic and CMP analysis, parasitic extraction, thermal and mechanical stress, and process modeling. Prior to the availability of the EDA 3D Analysis Suite, most commercial and internal EDA tool developers were faced with integrating their proprietary solvers into EDA applications with a combination of university and open source software, point tools and internally developed utilities-while having limited staff with expertise in 3D geometry modeling.

Spatial's EDA 3D Analysis Suite bridges the gap between 2D EDA physical databases and the input requirements of 3D solvers. The integrated software package is linked into EDA applications through a unified API that supports:

- 2.5D and 3D solid model creation and simplification
- Surface and solid meshing routines with robust meshing controls
- Platform-independent, high performance 3D display management

[Physware](#), a high-performance physics-based EDA design tool vendor, uses the EDA 3D Analysis Suite. According to Dr. Dipanjan Gope, Physware's Director of R&D, "Physware's technology provides high-speed, high-accuracy full-wave electromagnetic field solutions for SI, PI, and EMI issues in packages, boards, and systems-in-package. Spatial's EDA 3D Analysis Suite provides adaptive and controllable meshing, a valuable component to our overall solution. We are excited that partnering with Spatial accelerates our time to market."

The EDA 3D Analysis Suite is supported on 32- and 64-bit Linux, Solaris and Windows platforms. The company is demonstrating the EDA 3D Analysis Suite at the 45th annual Design Automation Conference in Anaheim, California June 8-13, suite #421. For further information on evaluation licenses, please visit <http://www.spatial.com>.

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## ***Synopsys Introduces HAPS-51T ASIC Prototyping System to Accelerate Time to Results***

30 May 2008

Synopsys, Inc. is introducing a new addition to the HAPS™ (High-performance ASIC Prototyping System™) product family from the company's Synplicity® Business Group. The HAPS-51T, leveraging the advanced capabilities of Xilinx's Virtex®-5 LX330T devices, is an ideal prototyping system for applications using high-speed serial interfaces like PCI Express, SATA and Gigabit Ethernet. The HAPS-51T utilizes the LX330T device's 24 RocketIO GTP transceivers, adding on-board DDR2 memory and the new HapsTrak high-speed daughterboard connectivity scheme in a compact form factor. These features enable HAPS-51T to deliver a significant advantage in performance and versatility.

The tight connection between the Virtex-5 LX330T FPGA and the on-board memory enables flexible,

high-speed memory access to satisfy even demanding communication applications. The HAPS-51T's on-board memory includes:

- 1GByte of DDR2, expandable to 8GByte
- 2Mbit x 36bit synchronous SRAM
- 32Mbit x 16bit Flash PROM

The expandable, modular architecture of Synopsys' HAPS systems offer many sophisticated features designed to appeal to system-on-chip (SoC) designers and software engineers alike. As with all HAPS systems, the HAPS-51T utilizes the HapsTrak standard, a set of guidelines for pinout and mechanical characteristics to help ensure compatibility with previous and future generations of HAPS motherboards and daughterboards. In addition, the HAPS-51T introduces the new HapsTrak MGB multi-gigabit SERDES bus with up to 8 lanes. Implemented in an 80-pin HapsTrak MGB connector it accommodates a variety of MGB daughter boards including:

- PCIE-4\_MGB: 4-lane PCI Express
- SATA-4\_MGB: 4-channel serial ATA
- ETH-4\_MGB: 4 Gbit Ethernet ports

Like all HAPS systems, the HAPS-51T is equipped with programmable clock generators, sophisticated monitoring and self-test features, as well as remote configuration and setup capabilities. Other integrated peripherals include:

- USB data port
- SelectMAP
- JTAG

"We continue to expand the HAPS family to address new applications and markets," said Juergen Jaeger, director, product marketing, Synopsys. "With the increased use of high-speed serial interfaces in today's communication and consumer designs, our customers asked for a prototyping solution to handle the specific verification requirements of high-performance serial interfaces. The HAPS-51T is a powerful, easy to use, expandable prototyping solution that was designed to meet this demand."

"We are pleased that Synopsys' Synplicity Business Group has chosen the Virtex-5 LX330T device for their newest HAPS prototyping system, the HAPS-51T," said Chuck Tralka, Senior Director of Virtex Product Marketing at Xilinx. "The combination of high logic density and high-speed serial transceivers in the LX330T device, combined with the flexibility of the HAPS system makes the HAPS-51T an ideal choice for development applications using high-speed serial interfaces."

## **Availability**

Shipments of HAPS-51T for early adopters have already begun. Full production shipment of the HAPS-51T is currently expected to begin at the end of June 2008.

For more information about the HAPS solution visit <http://www.synplicity.com/products/haps/>

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## ***Synopsys Releases Proven VMM Methodology Standard Library and Applications Under Apache Open Source License***

28 May 2008

Synopsys, Inc. announced it has released the source code for its complete implementation of the VMM verification methodology for SystemVerilog, including the VMM Standard Library and VMM Applications, under the popular Apache 2.0 open source license. Synopsys' implementation of the VMM methodology, originally defined in the Verification Methodology Manual for SystemVerilog, was recently donated to the Accellera standards organization to speed development of verification interoperability standards. By releasing its implementation of the VMM methodology, Synopsys has enabled users and vendors from across the industry to take immediate advantage of the years of investment in VMM methodology development as Accellera begins its standardization efforts. Synopsys' complete implementation of the VMM methodology is available as a free download at <http://www.vmmcentral.org/>.

"With the daunting complexity of today's SoCs and their associated testbenches, it's increasingly important that development teams deploy re-usable Verification IP that is designed to interoperate at the SoC level," said Paul Tobin, director of AMD's Verification Center of Expertise. "Synopsys' open sourcing of VMM and its donation to Accellera for consideration by the VIP technical subcommittee are positive steps towards the standardization and open interoperability between EDA tools that users need."

"By releasing its VMM implementation under the Apache license, Synopsys is inviting the entire industry to use the same proven methodology successfully deployed on hundreds of projects around the world," said Manoj Gandhi, senior vice president and general manager of the Verification Group at Synopsys. "This move supports Accellera's verification interoperability objectives by enabling users to more easily share VMM environments and components as Accellera develops a verification interoperability standard."

Download, Learn and Participate at VMM Central

VMM Central is a comprehensive online resource for designers using the VMM methodology. Synopsys' complete implementation of VMM, which is available for free download at <http://www.vmmcentral.org/> includes:

- VMM Standard Library
- VMM Register Abstraction Layer application
- VMM Reusable Environment Composition application
- VMM Memory Allocation Manager application
- VMM Hardware Abstraction Layer application
- VMM Data Stream Scoreboard application
- VMM Macro Library

A variety of other resources are also available at VMM Central to help improve productivity for both new and existing VMM users, such as:

- Documentation for the VMM Standard Library and VMM Applications
- VMM utilities, including RALGEN and VMMGEN, for rapid environment development

- Example VMM environments
- HVP verification planning language specification
- User papers and technical articles
- VMM user discussion forum
- "Verification Martial Arts" blog by Janick Bergeron, Synopsys fellow and co-author of the Verification Methodology Manual for SystemVerilog

## **Proven and Trusted VMM Methodology**

The VMM methodology has been successfully deployed by hundreds of project teams across the globe since its release in 2005, and over 50 technical papers and tutorials on the VMM methodology have been presented at Synopsys Users Group (SNUG®) meetings and other venues. Companies that have used VMM to address their verification challenges and/or have contributed to VMM include: Alacritech, AMD, Analog Devices, ARM, Axis Communication, Broadcom, Commex Technologies, CreVinn Teoranta, Denali, Emulex, Enterasys Networks, Ericsson, Faraday Technology, Fore River Group, General Dynamics, HCL Technologies, HiSilicon Technologies, Innovative Logic, Integrated Device Technology, Intellon, ITT Corporation, Jen2, LOA Technologies, Mindtree Consulting, Neterion, NextIO, OKI Network LSI, Patni, Ross Video, Silicon Logic Engineering, Solarflare Communications, STARC, Sun Microsystems, SyoSil, Tego, Texas Instruments, Transnoma Medical, Transwitch India, Verivue, Virident Systems, Wavesat Telecom, Wipro Technologies, and more.

## **VMM Ecosystem Based on Broad Vendor Support**

A large number of EDA suppliers, consultants, and training organizations throughout the industry are able to leverage VMM technology and expertise to create tools, verification IP (VIP), training, and services for chip development teams through the VMM Catalyst Program. More than 60 companies are currently members of the VMM Catalyst program, including: Aldec, Avery Design Systems, Blue Pearl Software, Bluespec, Callidus Systems, Certess, ChipRight, Computer Based Education, Contemporary Verification Consultants, Doulos, eInfochips, EVE, JEDA Technologies, Kacper Technologies, Krispan, Masamb Electronics Systems, Mimasic, NoBug, Novas Software, nSys Design Systems, Paradigm Works, Perfectus Technology, Productivity Design Tools, Silicomotive Solutions, Silicon Interfaces, Star-Mountain, Sunburst Design, Sutherland HDL, Synterix, ThurstIC, TransEDA, UV-Tech Consulting & Training, VeriEZ Solutions, Verific Design Automation, Verification Central, Verifore, VeriSure Digital, Winterlogic, XtremeEDA, Zocalo Tech, and more.

## **VMM User Forum at DAC**

Synopsys is sponsoring a VMM User Forum luncheon at the Design Automation Conference (DAC) in Anaheim, Calif. on June 10, 2008. Join Synopsys and VMM users to explore how VMM has extended methodology beyond base classes. For more information and to register, please visit <http://www.synopsys.com/dacvmm/>. The VMM methodology will also be featured in Synopsys' main booth at DAC (booth # 1349) and in the Synopsys Standards Booth at DAC (booth # 1541).

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## ***Synopsys Unveils New IC Compiler Router Delivering 10X Speed-Up***

27 May 2008

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[Synopsys, Inc.](#) unveiled Zroute, a new multi-threaded router fully integrated into IC Compiler. Driven by leading-edge early users in the microprocessor, consumer, wireless and computer-graphics markets, Zroute has been developed from the ground up to take full advantage of the newest multi-core microprocessor architectures and to solve emerging design-for-manufacturability (DFM) challenges in IC design.

Zroute's modern architecture incorporates state-of-the-art routing technology, such as native support of soft rules to enable "lithography-friendly" routing and avoid manufacturing problems. By simultaneously considering the impact of manufacturing rules, as well as timing and other design goals, Zroute delivers high quality of results (QoR) and improved manufacturability. Zroute was developed to take advantage of modern multi-core compute platforms. Utilizing a combination of advanced routing algorithms and multi-threading technology, Zroute has shown a speed increase of more than 10X on customer designs running on quad-core machines.

"With its multi-threaded capability and its flexibility to dynamically support both on-grid and off-grid routing, Zroute is very well-positioned to meet our needs for faster design closure," said Michael Shiuan, vice president of Engineering at S3 Graphics. "In our trials, Zroute converged 3.3 times faster when single-threaded and achieved a 10X boost in performance when we used it with a quad-core platform."

Zroute is incorporated as a standard feature in IC Compiler, offering an alternate choice for routing technology which can be enabled by customers as required. Zroute was specifically developed as a concurrent optimization router to deal with future technical challenges. Rather than addressing these issues later in the flow, Zroute's strategy reserves routing resources for yield optimizations at each step of the flow, enabling their impact to be considered simultaneously with other cost functions. Additionally, Zroute is multi-threaded at each of its internal steps. Its routing engines have demonstrated near-linear scalability of runtimes as the number of threads increases, promising significant speed-ups for IC Compiler customers transitioning to four- or eight-core platforms and beyond.

"Zroute is an excellent example of Synopsys' investment in R&D to help our customers stay ahead of the technology curve. Anticipating future requirements, we set out to develop a new router that not only could address the emerging issues of DFM, timing, and other design goals, but could do it much faster executing transparently on the new multi-core processors," said Antun Domic, senior vice president and general manager, Synopsys Implementation Group. "Zroute has been very successful with early adopters, and we're looking forward to bringing this exciting technology to all IC Compiler users."

Zroute will be in limited production availability as a standard feature in IC Compiler in June 2008.

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## ***Trubiquity Names First Sales Partner in China***

27 May 2008

Trubiquity (formerly Autoweb), has named PDS Technology Company Ltd. as its first sales partner in China.

PDS, one of the largest resellers of engineering design software in Southwest China, will provide sales, service and marketing support for Trubiquity's product portfolio throughout China.

"It is critical that we continue to broaden our global reach to include the Asian markets," said Nino DiCosmo, president and CEO of Trubiquity. "PDS marks the first step in expanding our product

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offerings into Asia.”

Trubiquity’s sales partnership program consists of value-added resellers (VARs) selected to sell Trubiquity’s software-based solutions as well as complementary services to enhance overall product offerings for customers. The company currently has more than 80 global sales partners in existing programs in Europe and North America.

“We are excited to have PDS as part of our global service delivery capability,” said Steve Koons, senior vice president of Business Development for Trubiquity. “In addition, PDS will provide necessary insight into local business requirements in order to better tailor our global capabilities.”

The Trubiquity product portfolio consists of software that streamlines and automates customers’ internal business processes including data exchange and management, team collaboration, electronic procurement, commercial EDI and quality/measurement. In addition, Trubiquity’s software solutions speed product planning, design and manufacturing while reducing costs, improving time-to-market and ensuring quality across the product lifecycle.

Headquartered in Chengdu, China, with a sales office and training center in Chongqing, PDS provides technology products, services and solutions to more than 40 businesses in Southwest China as well as offers professional C3P and product lifecycle management (PLM) solution training to an additional 100 customers in China. PDS also is a premier business partner of Siemens PLM Software in China.

“We are pleased to partner with Trubiquity in China,” said John Li, president of PDS Technology Company Ltd. “We look forward to teaming with Trubiquity and expanding the capabilities we offer to our valued customer base.”

Based in Rochester Hills, with European headquarters in Germany and the U.K., Trubiquity is a leading provider of business process automation and global data exchange software for manufacturers and suppliers. The company provides a single, software-as-a-service-based business process network connecting enterprises across all global communication protocols and standards. Trubiquity’s solutions can be accessed all the time, everywhere around the globe.

[Trubiquity](#) has more than 5,200 customers in 40 countries, with more than 80 global sales partners.

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## ***Virage Logic Speeds Time-to-Market with an All-Digital, High-Performance DDR2/3 PHY+DLL Solution***

28 May 2008

[Virage Logic](#) Corporation announced its Intelli™ DDR2/3 PHY+DLL, an all-digital PHY+DLL high performance DDR solution. Supporting speeds of up to 1066 Mbps in 65-nanometer (nm) G processes, the all-digital Intelli DDR2/3 PHY+DLL achieves performance and resolution levels that were previously only possible with analog solutions. Unlike all-digital solutions, analog solutions mandate costly silicon validation, with potential silicon re-spins, and therefore typically translate into longer time-to-market.

Virage Logic's Intelli DDR2/3 controller and PHY+DLL provide a flexible and advanced solution ideal for application specific integrated circuit (ASIC) and System-on-Chip (SoC) designers requiring a reduced area, high performance DDR memory interface. Delivered in an all-digital solution, the Intelli DDR2/3 controller and PHY+DLL are a low-risk investment, providing designers with benefits such as

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faster time-to-market, higher data throughput efficiency, lower system costs, less power usage and a superior first-time silicon success rate.

"As the semiconductor industry's trusted IP partner, designers trust Virage Logic to deliver superior IP solutions and our high performance DDR2/3 PHY+DLL in an all-digital implementation definitely lives up to this high standard," said Kamalesh Ruparel, vice president and general manager of Virage Logic's application specific IP (ASIP) business. "The Intelli DDR2/3 PHY+DLL not only delivers very high resolutions with an all-digital implementation, but it also provides a significant time-to-market advantage because its standard-cell architecture enables easy design portability across different foundries and process nodes with a high first-time silicon success rate. In contrast, traditional analog-based solutions mandate silicon validation, with potential silicon re-spins, to ensure correct functionality."

Because of its unique patent-pending architecture, the all-digital Intelli DDR2/3 PHY+DLL is able to achieve the resolutions required to support data rates of up to 1066 Mbps on 65nm G processes, making it a versatile solution for reduced power in a broad range of high performance applications including networking, video, graphics and portable electronics. Its standard cell architecture and digital implementation enable it to integrate seamlessly with digital SoC design flows and allow significant ease of portability to any process node for any foundry.

The Intelli DDR2/3 controller's advanced architecture allows much higher data throughput efficiency than older architectures provided by competing solutions. Such high efficiencies enable designs to operate at lower frequencies, resulting in lower power consumption and bill of materials costs, which translates into lower system costs.

The Intelli DDR controller and PHY+DLL support single data rate JEDEC standard SDRAM and Mobile SDRAM, double data rate JEDEC standard DDR1/2, DDR2/3, and MobileDDR as well as various multi-protocols.

## **Pricing and Availability**

The Intelli DDR2/3 PHY+DLL all-digital solution for the 65nm G process is available now with pricing starting at \$180,000.

## About Virage Logic's Application Specific IP (ASIP) Solutions

Virage Logic's ASIP product portfolio is designed to meet the highest quality and performance standards for functional IP and contains application specific functional IP targeted to a variety of market requirements for high performance, low power, and low gate count. The ASIP products are commonly based on Virage Logic's proprietary and patented logic libraries, routing methodology, and cell architecture.

Virage Logic's Intelli™ DDR memory interface solutions include a memory controller and the related PHY and DLL with advanced features to deliver high performance in a minimum die area and with low power dissipation. The solutions are architected to enable optimal size while providing customers with the ability to achieve the maximum performance and minimal power targets with the added flexibility of advanced feature selection. The Intelli DDR solutions are implemented in several SDR and DDR protocols, including SDRAM and Mobile SDRAM, as well as MobileDDR, DDR1 and DDR2 at several foundries on a variety of process nodes.

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